

1/55

FIG. 1

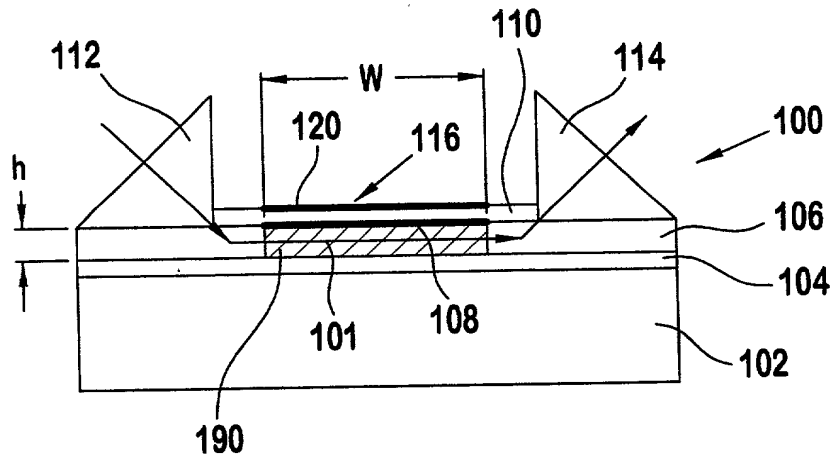


FIG. 2

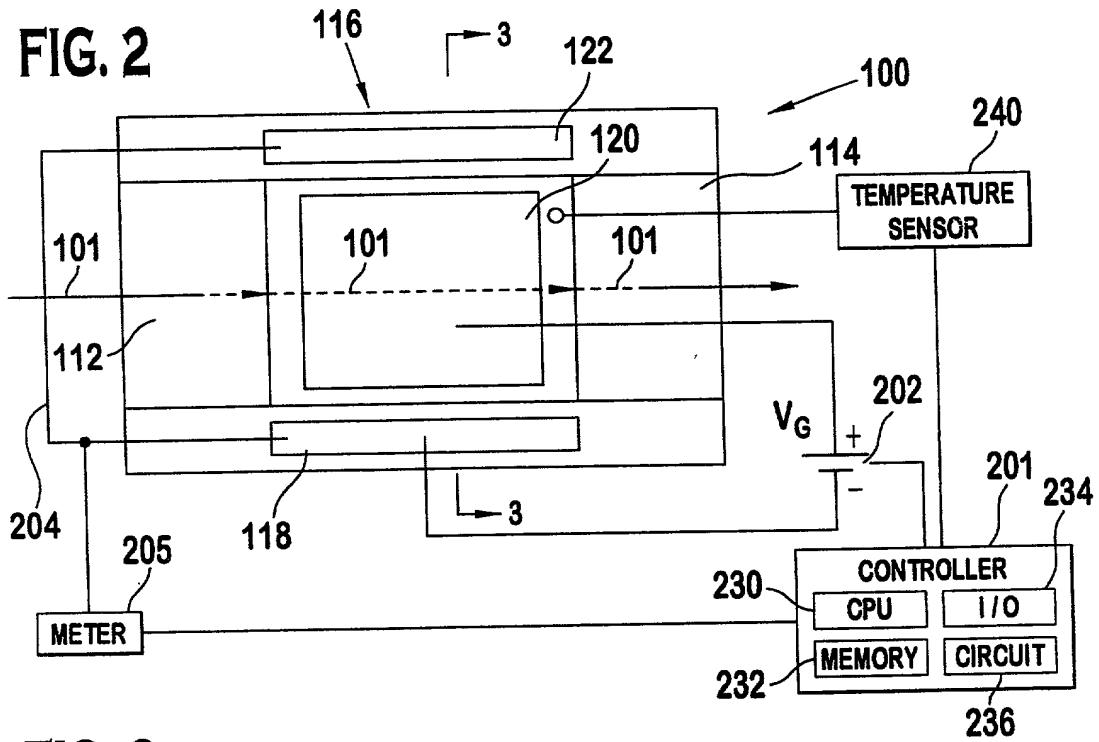


FIG. 3

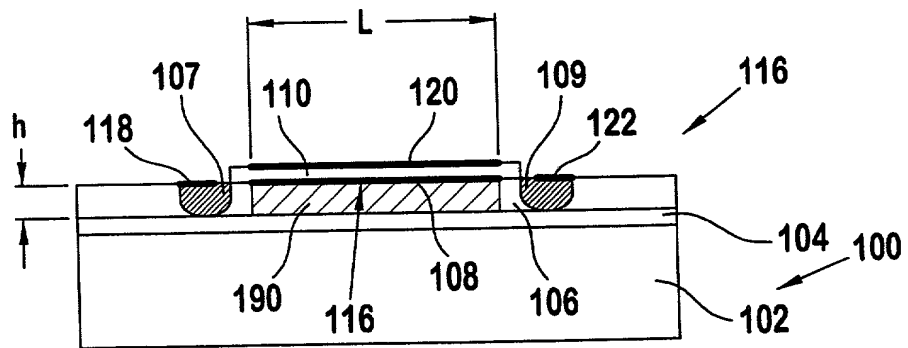


FIG. 5

The diagram illustrates a device 500 in cross-section. It consists of a substrate 502 with a layer 504 on top. A central region 505 is defined within layer 504. A gate 118 is positioned above the central region 505, and a gate voltage V_G is applied to it. A current source 508 is connected to the central region 505. A controller 201 is connected to a meter 205 and a temperature sensor 240, which are in turn connected to the device. A current source 508 is also connected to the device. The device is labeled 500 at the bottom left.

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FIG. 6

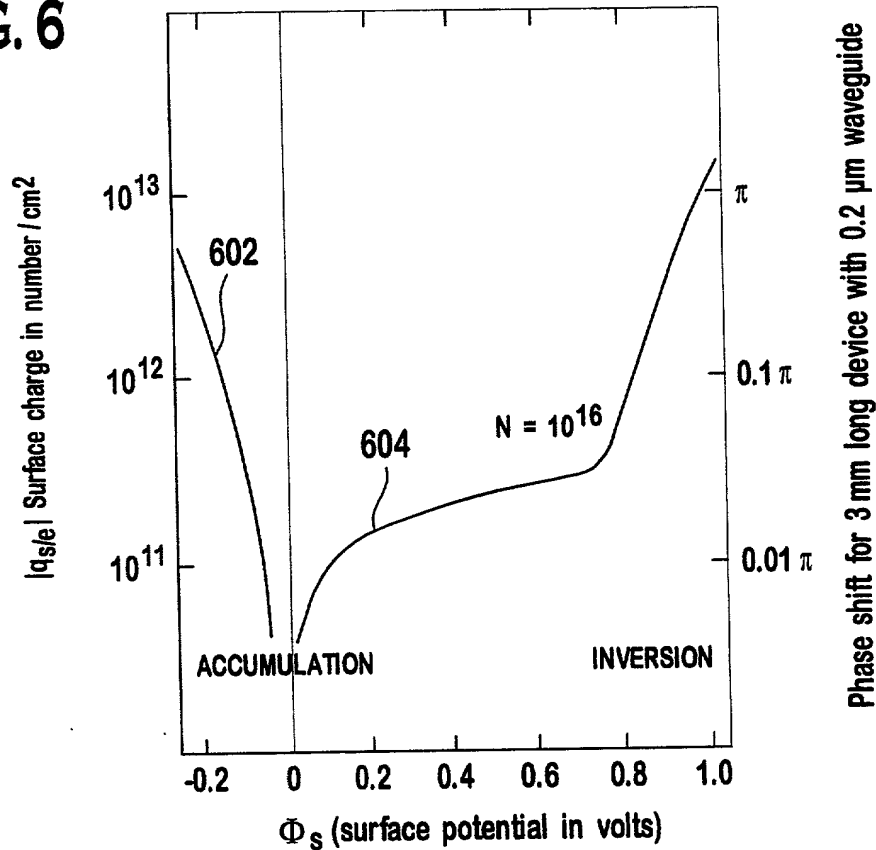


FIG. 7

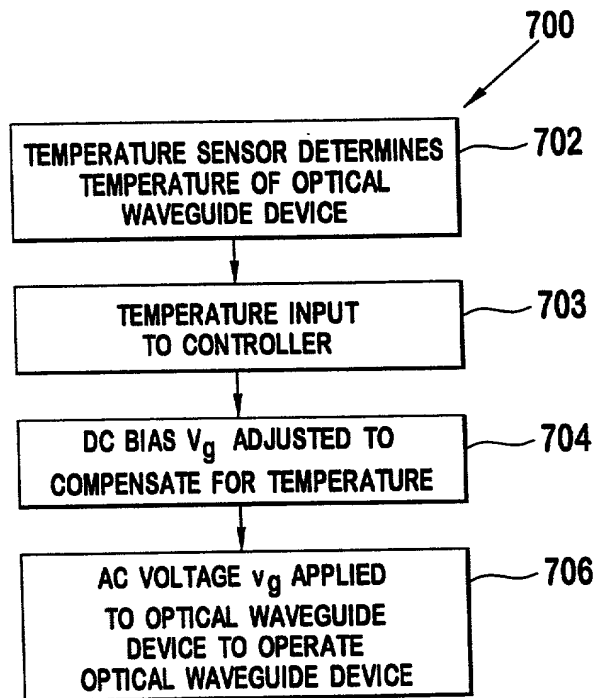


FIG. 8

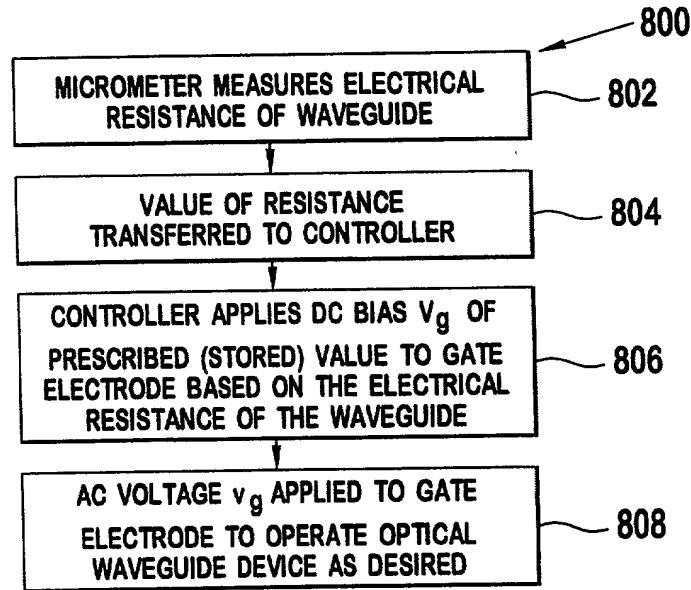


FIG. 9

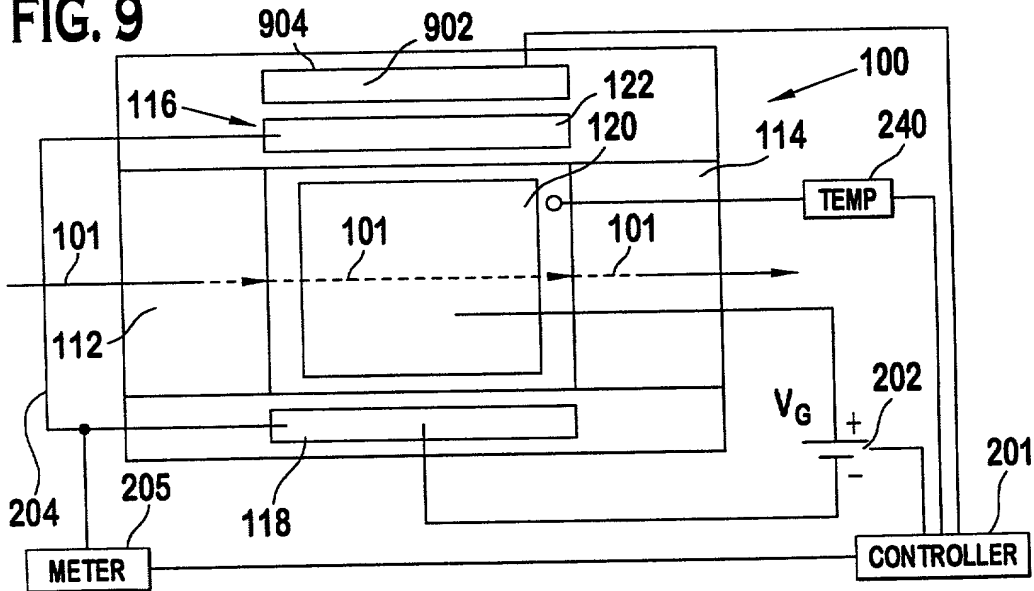


FIG. 10

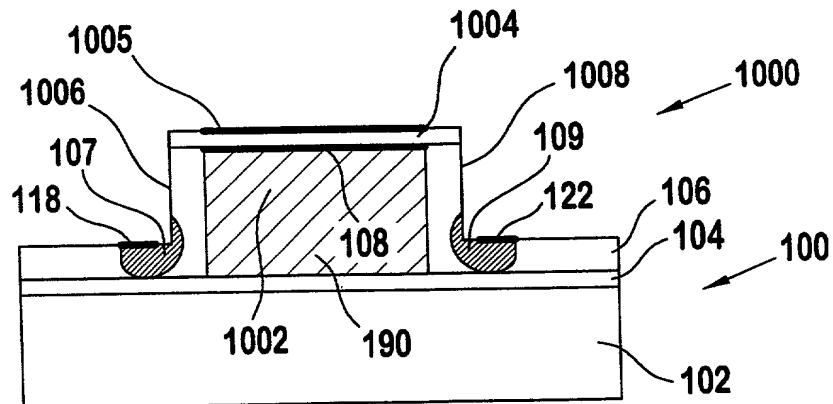


FIG. 12

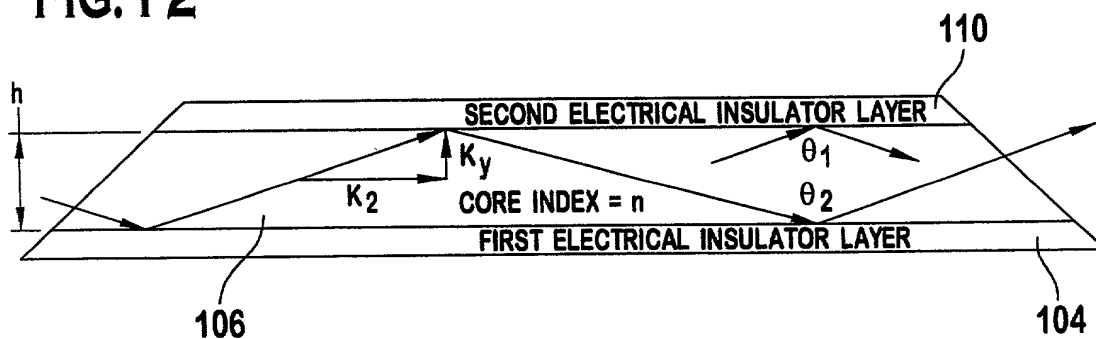
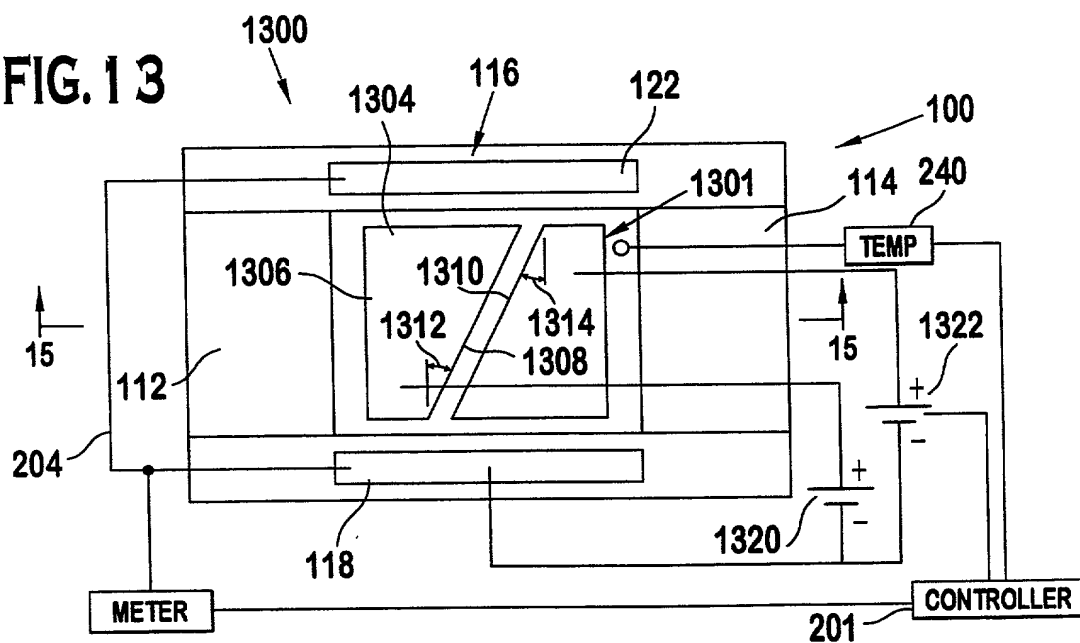
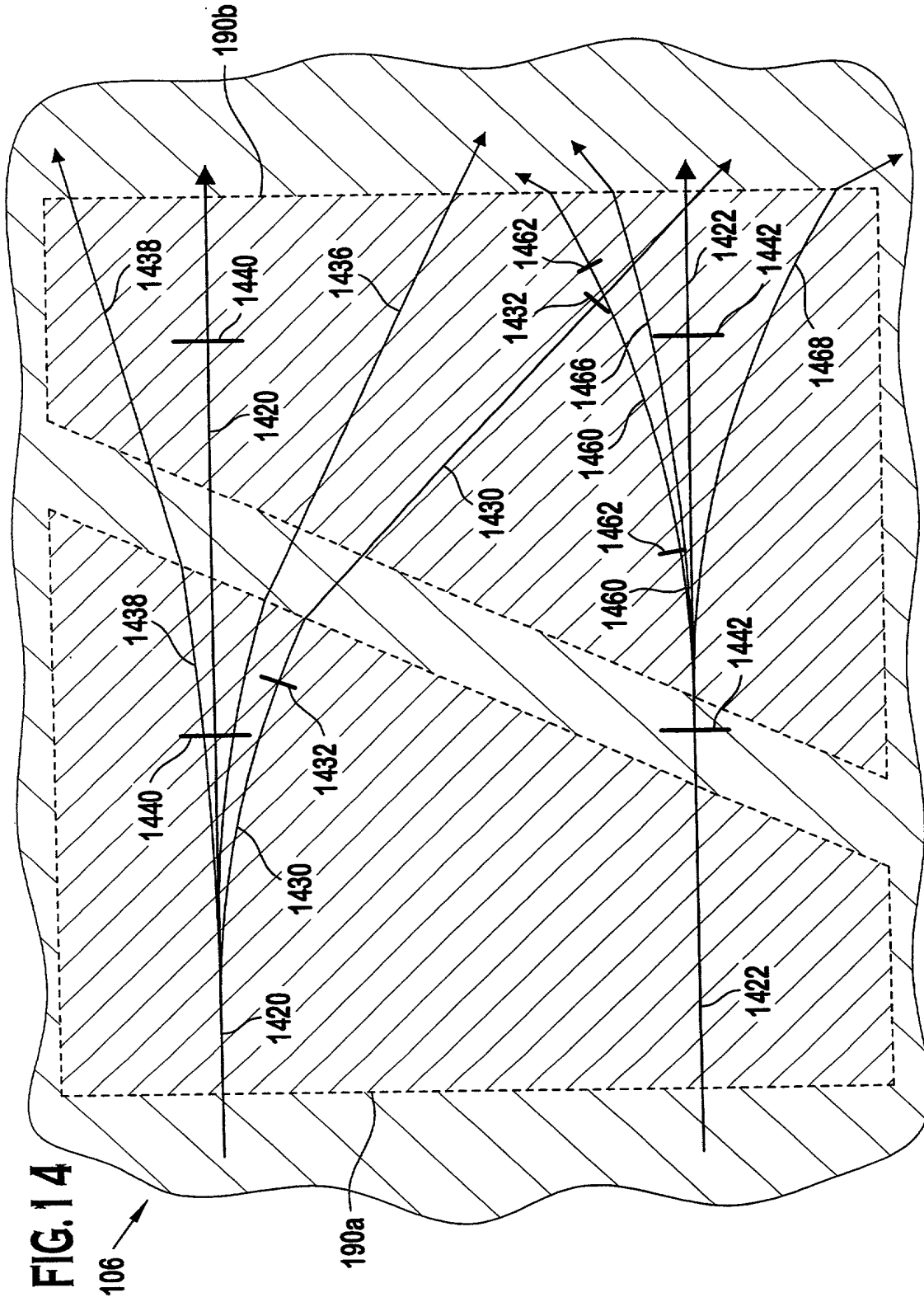


FIG. 13



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FIG. 14



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FIG. 15A

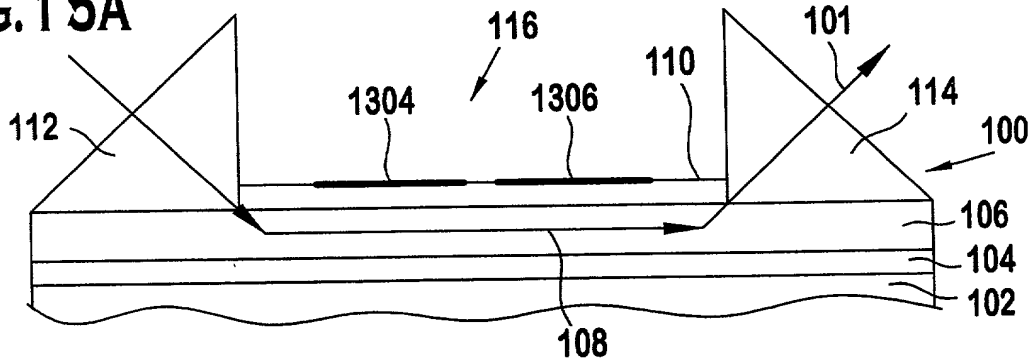


FIG. 15B

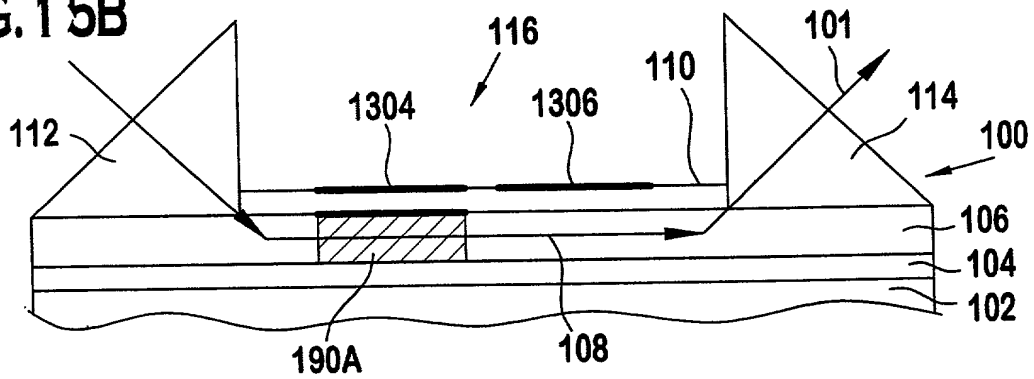


FIG. 15C

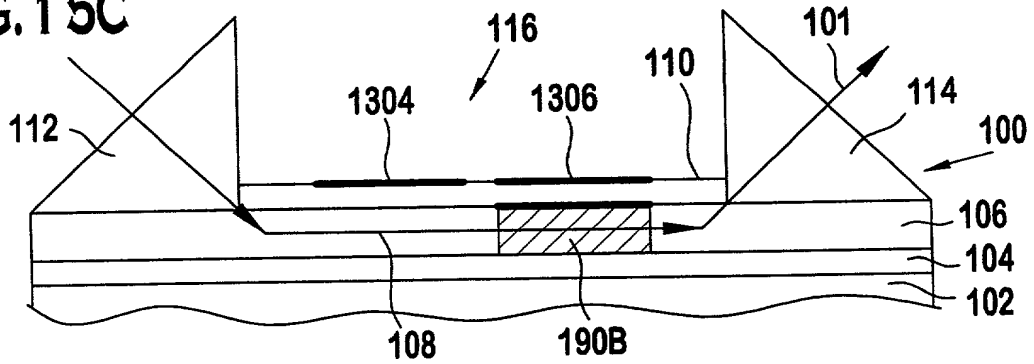
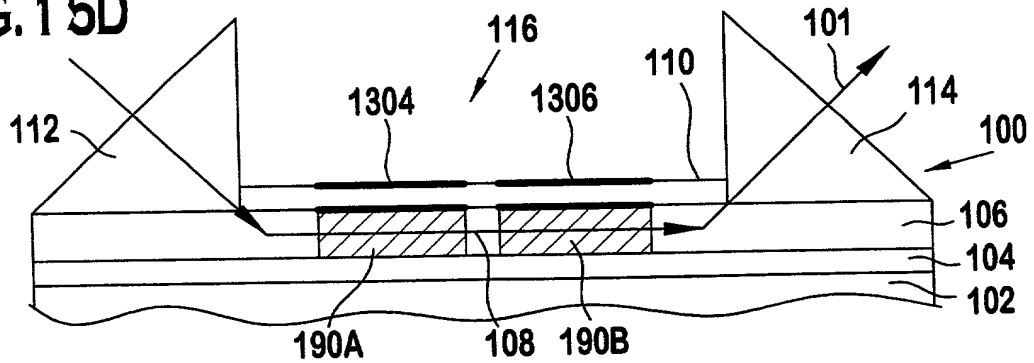


FIG. 15D



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FIG. 16

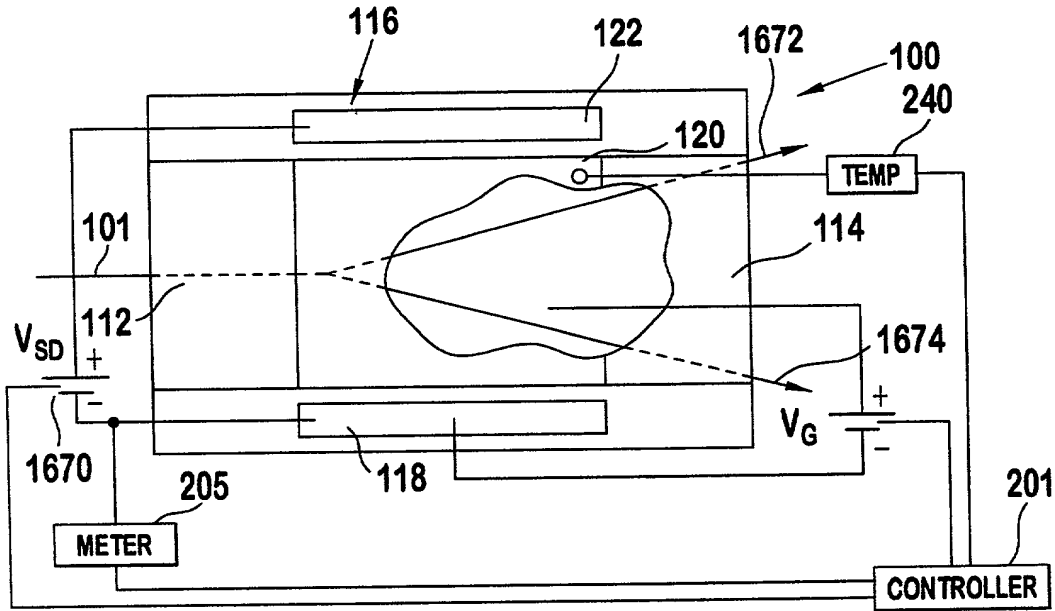
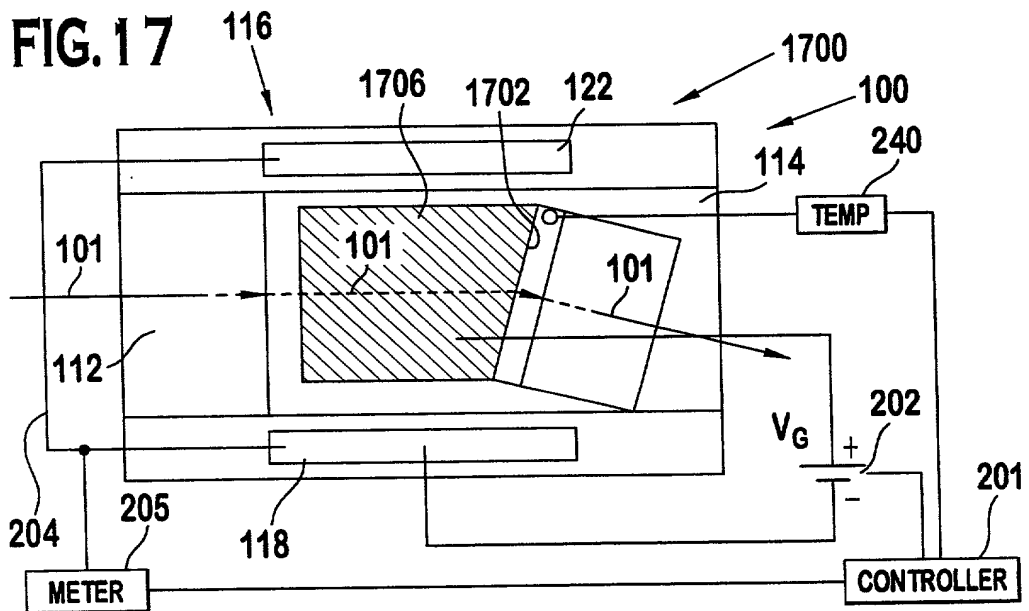


FIG. 17



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FIG. 19

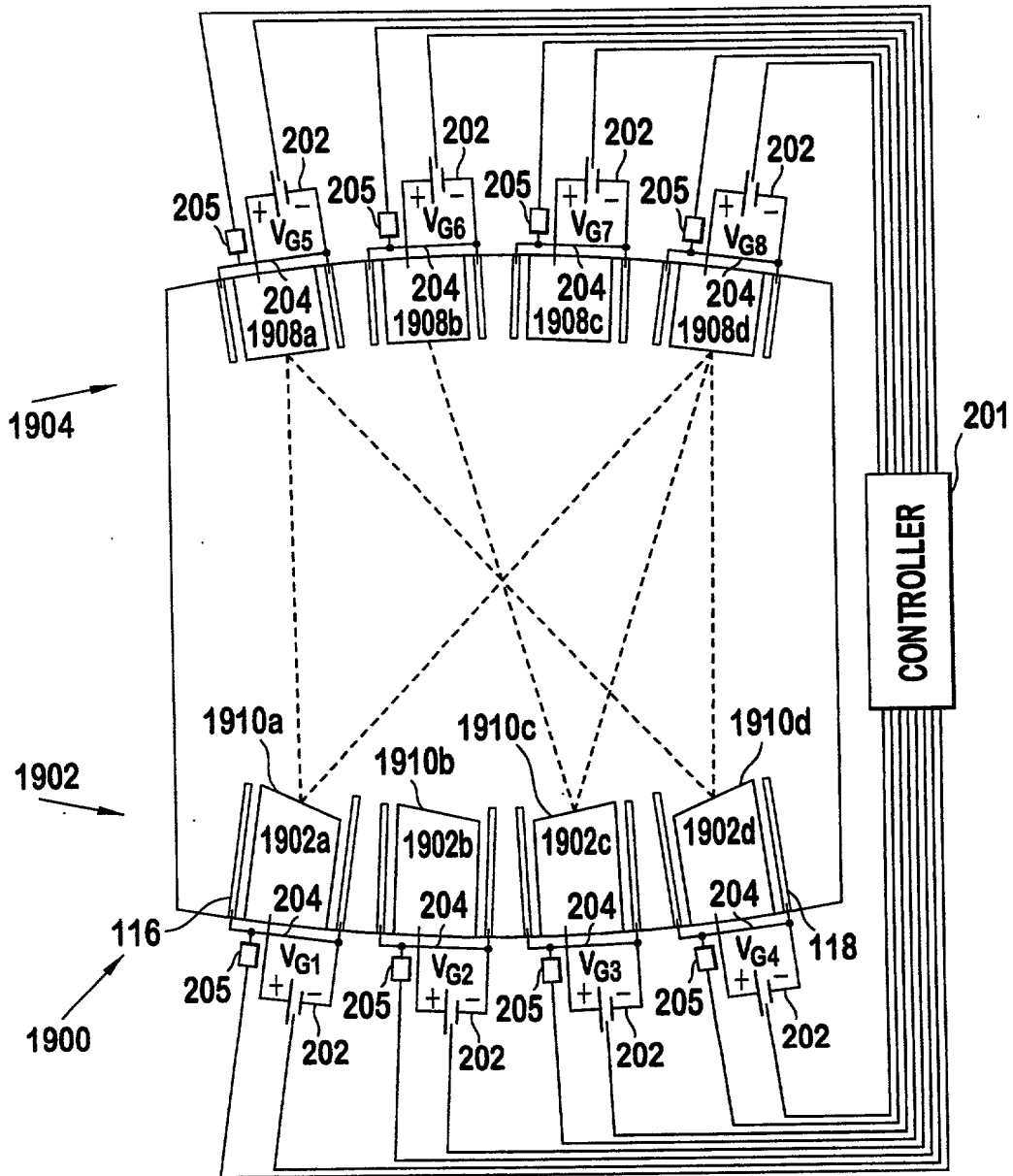


FIG. 20

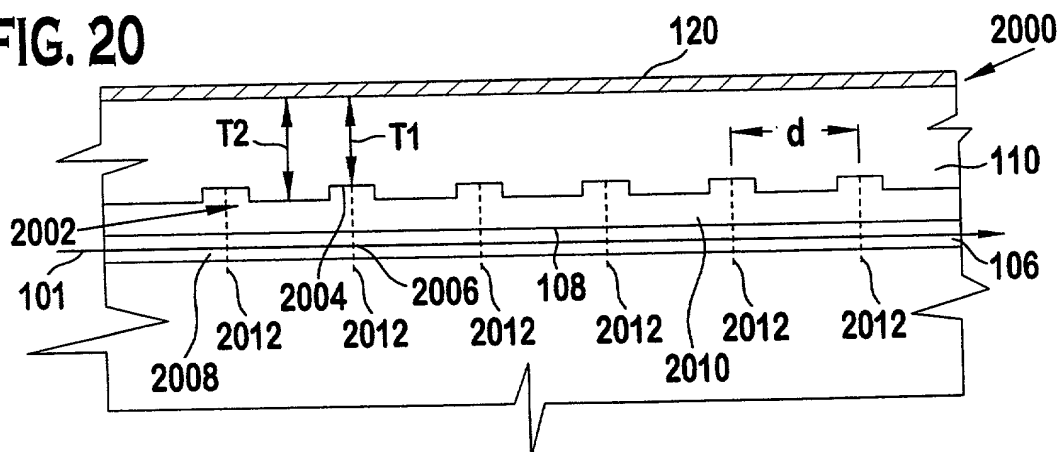


FIG. 21

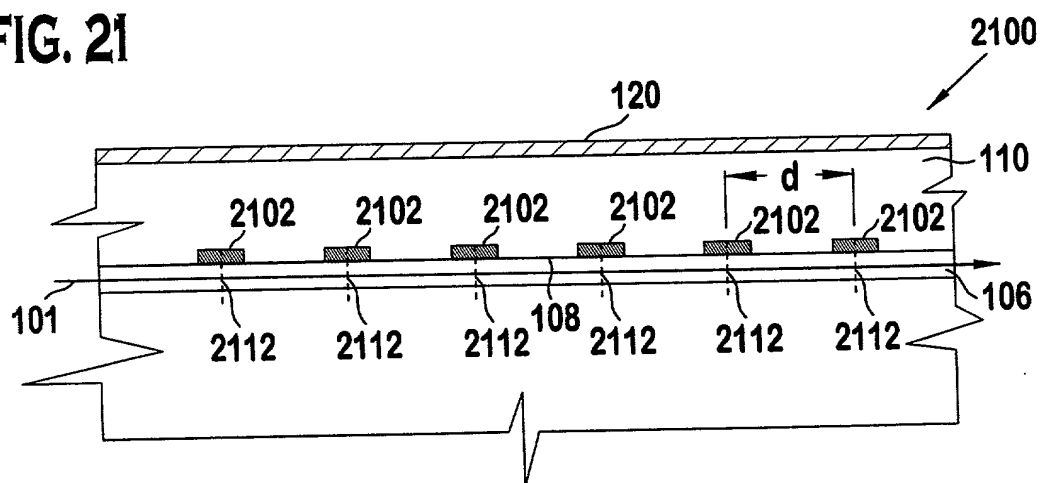


FIG. 22

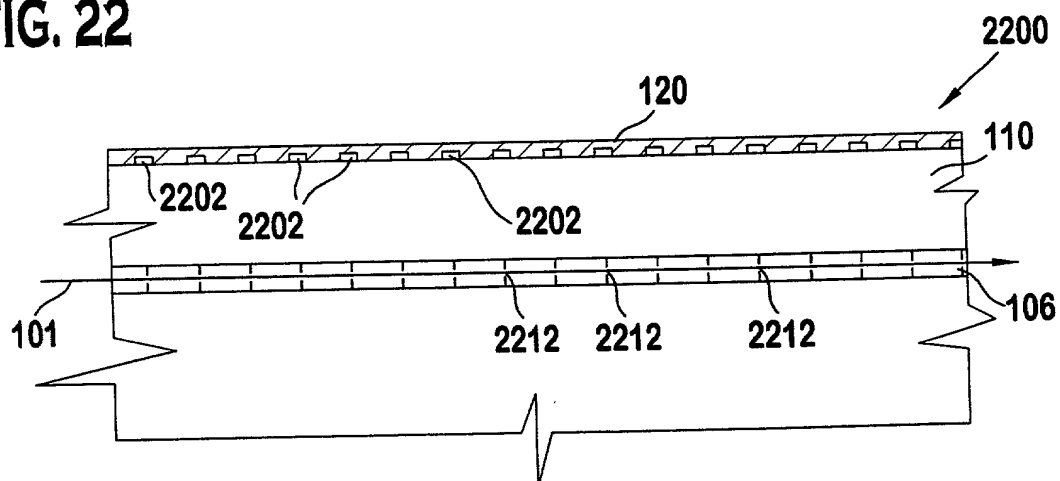


FIG. 23

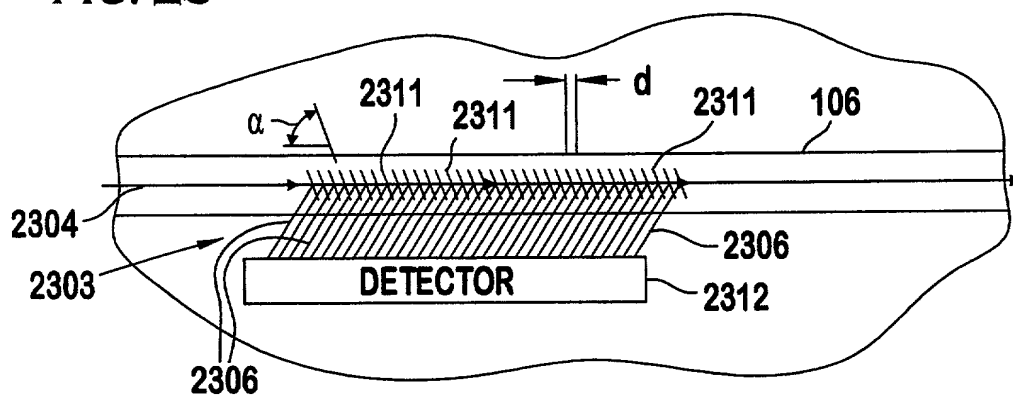


FIG. 24

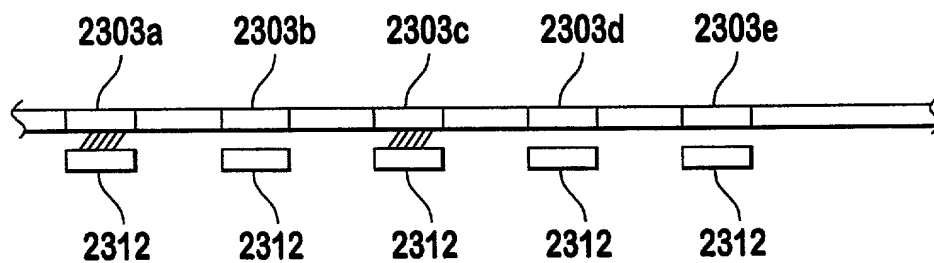


FIG. 25

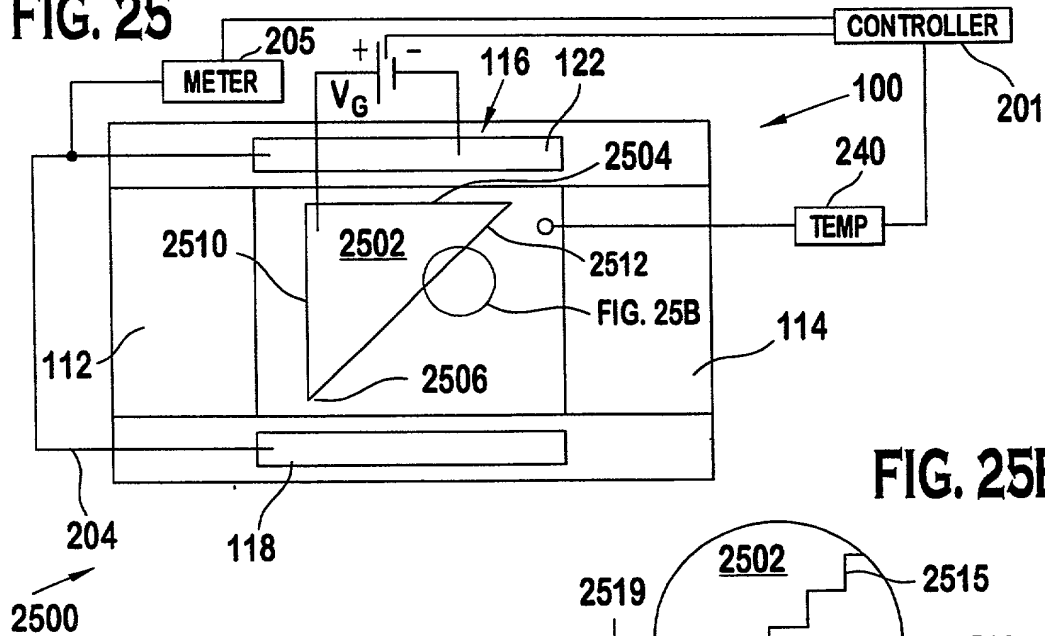


FIG. 25B

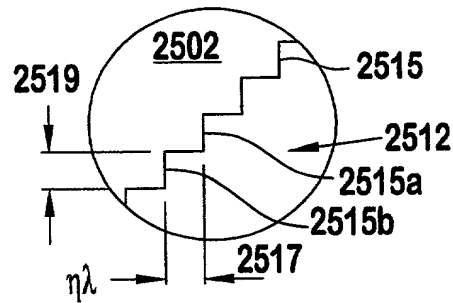


FIG. 26

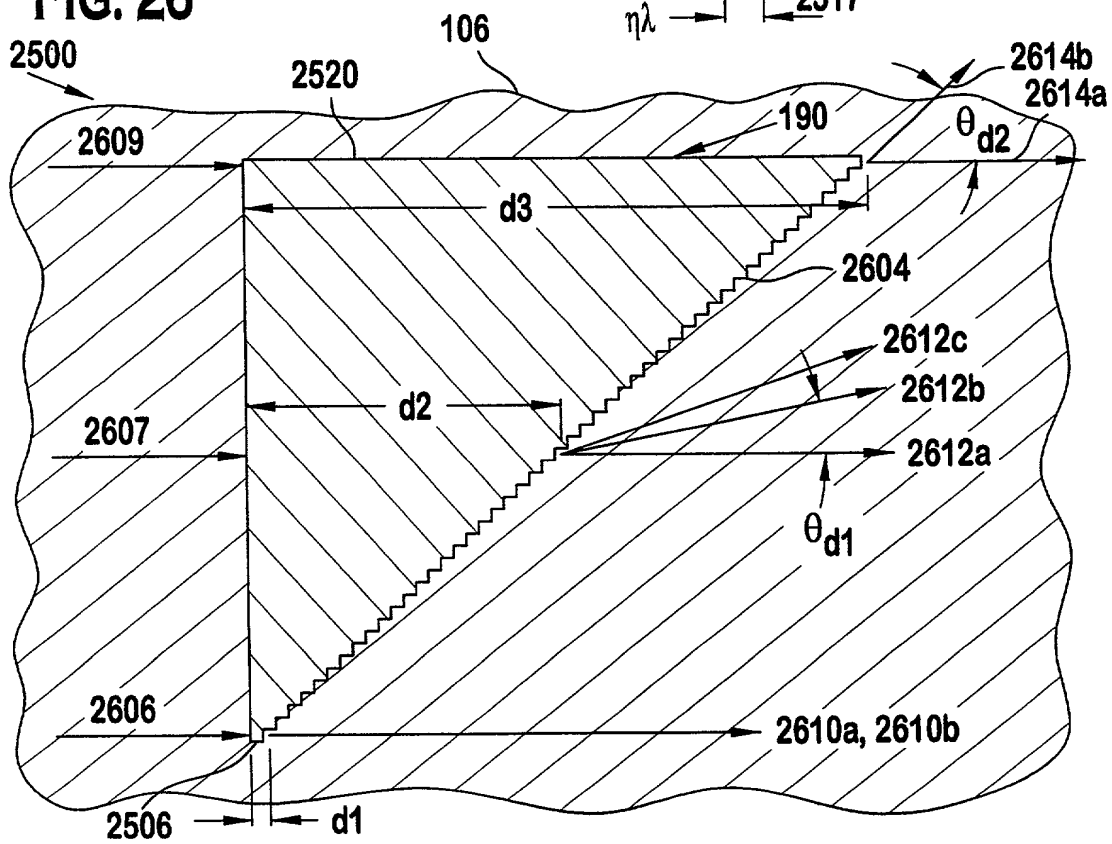


FIG. 27

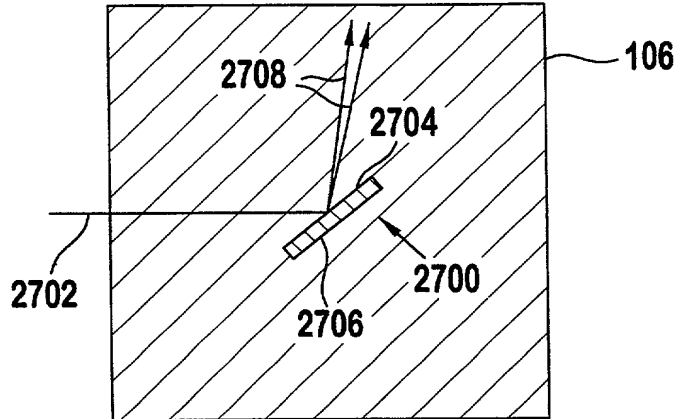


FIG. 28

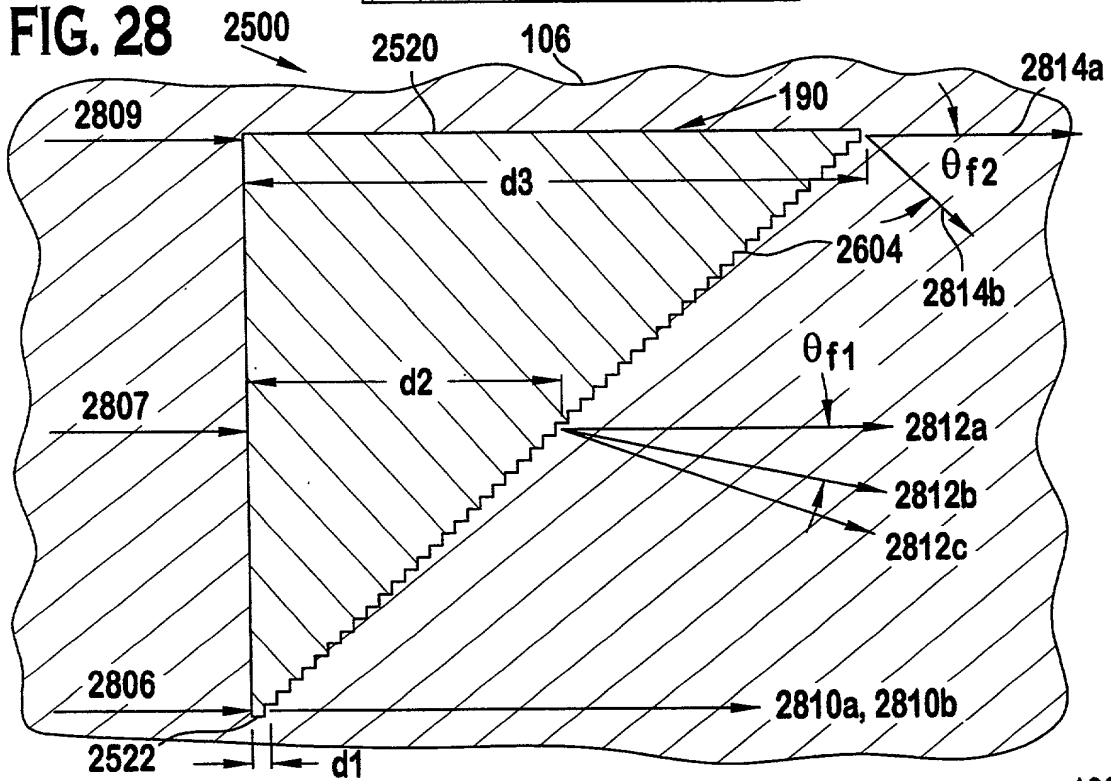


FIG. 29

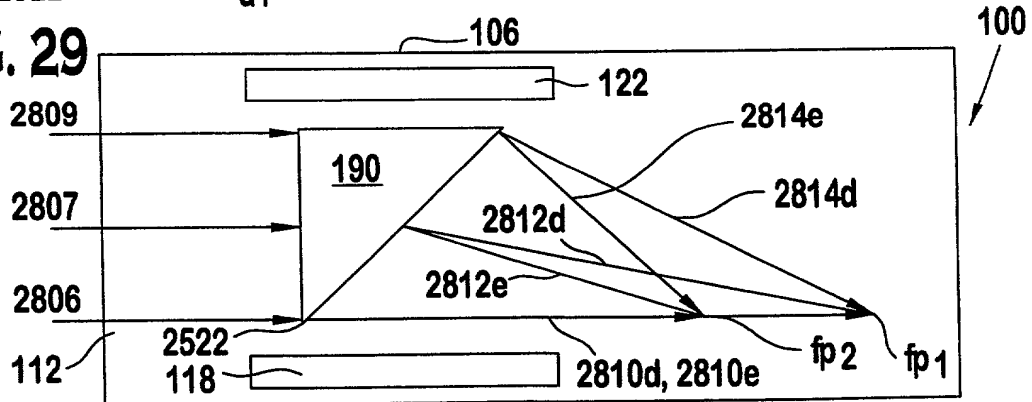


Diagram illustrating a light source assembly 3008. The assembly includes a lens 3012a and a light beam 3014a. The lens 3012a is shown as a vertical rectangular element with a cross-hatched pattern. The light beam 3014a is shown as a cone of light originating from the lens and converging at a point 3016. The beam is labeled 3014a and 3014b. The lens is labeled 3012a and 3012b. The light source assembly is labeled 3008. The beam is labeled 3016.

[illegible]

A cross-sectional view of a device. A central vertical stack of alternating shaded and unshaded rectangular layers is labeled 190. An upward-pointing arrow is positioned above this stack. The stack is flanked by two regions: a rectangular region on the left labeled 3212a and a tapered region on the right labeled 3214a. The bottom of the device is defined by a horizontal line labeled 3212b, and the bottom of the tapered region is labeled 3214b. A point on the right edge of the tapered region is labeled 3016. A reference numeral 3208 with an arrow points to the left side of the device.

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FIG. 33

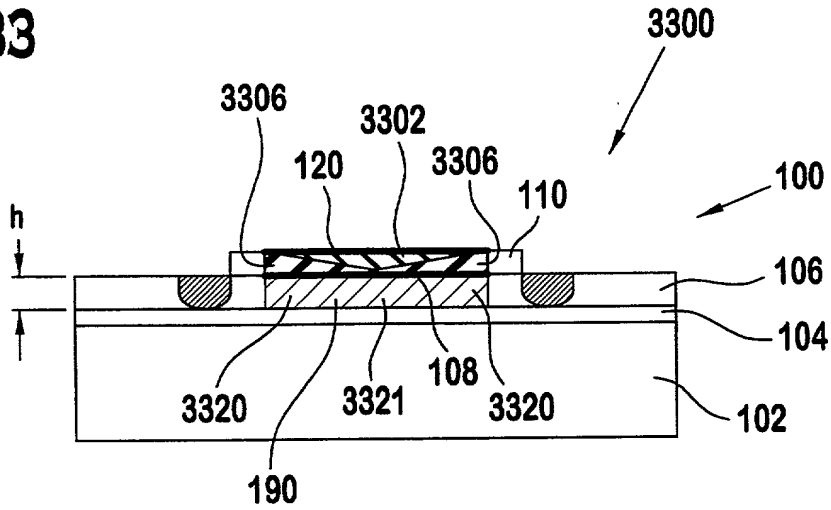
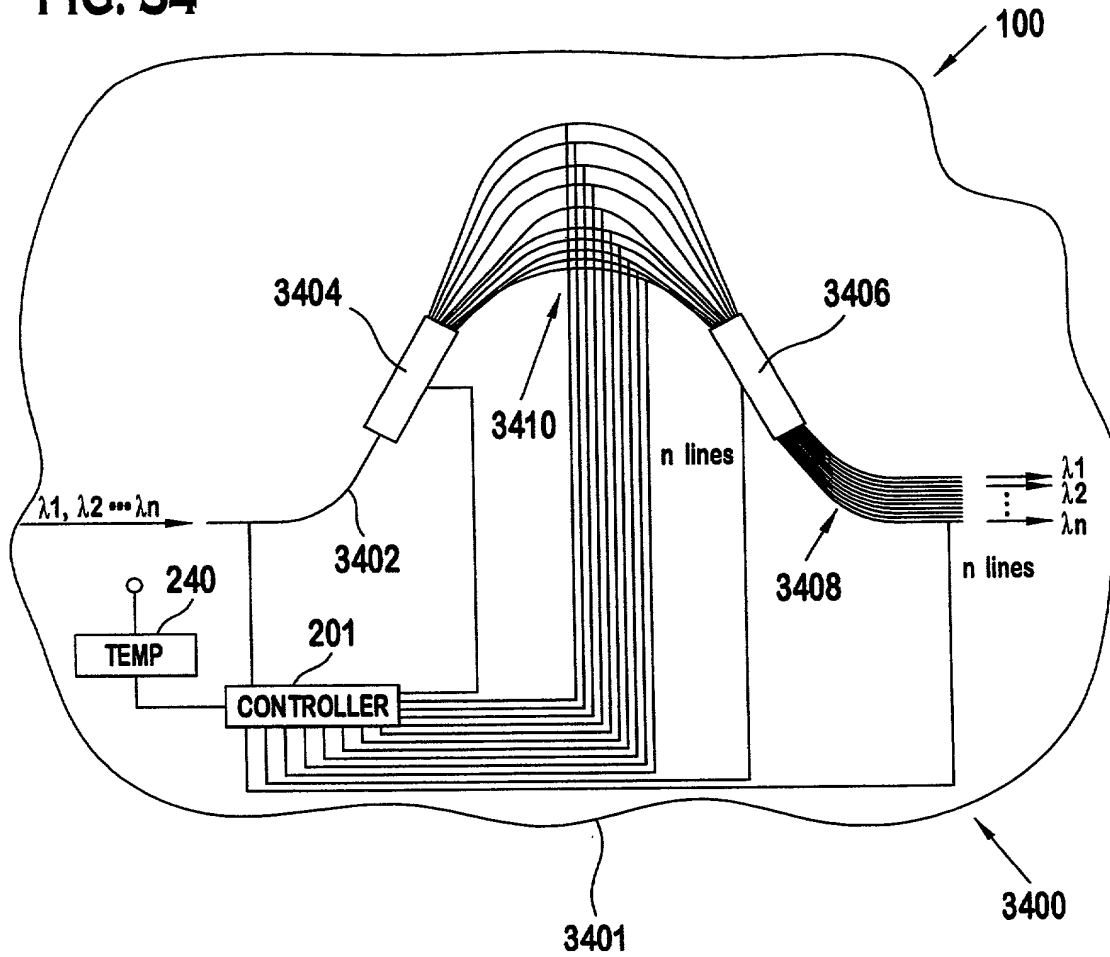


FIG. 34



[illegible]

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FIG. 37

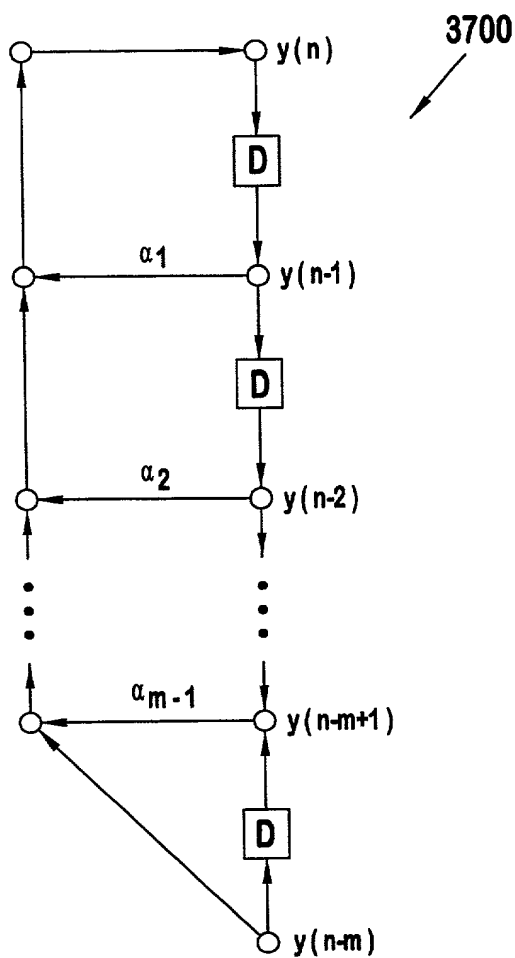
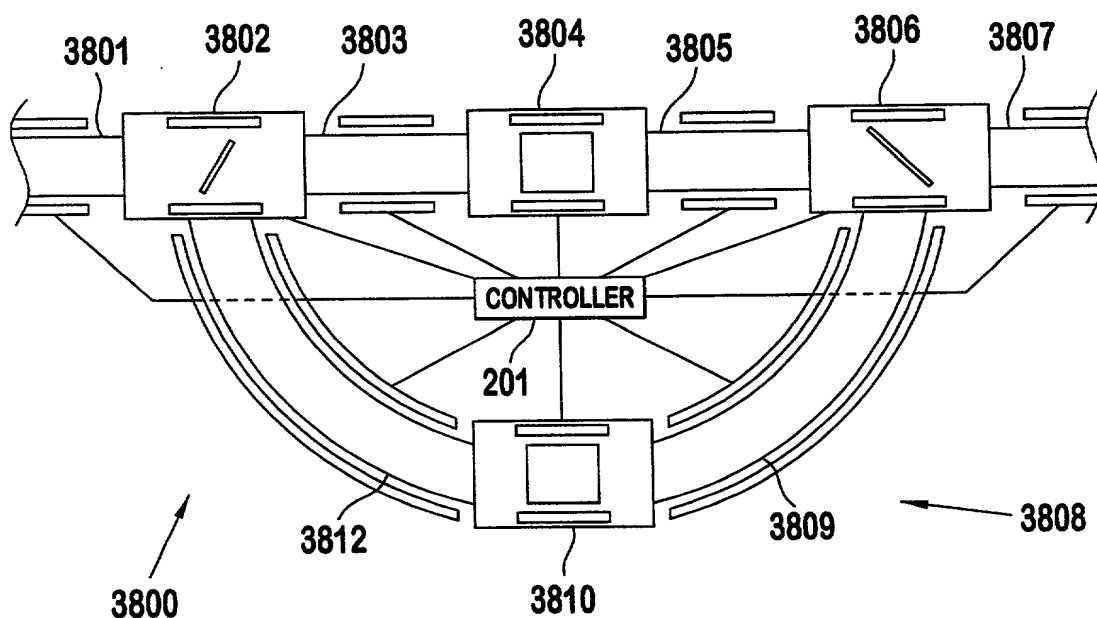
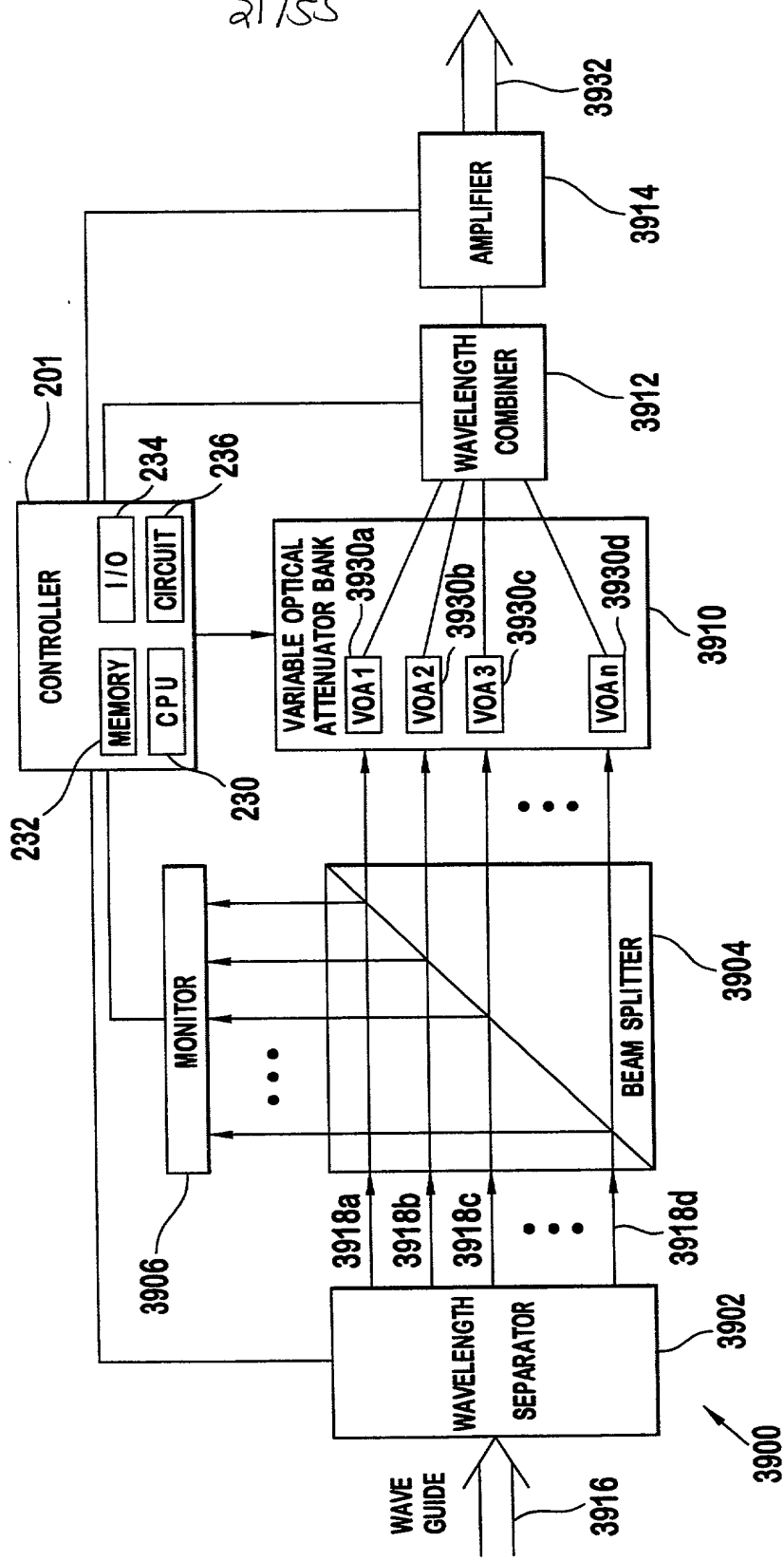


FIG. 38



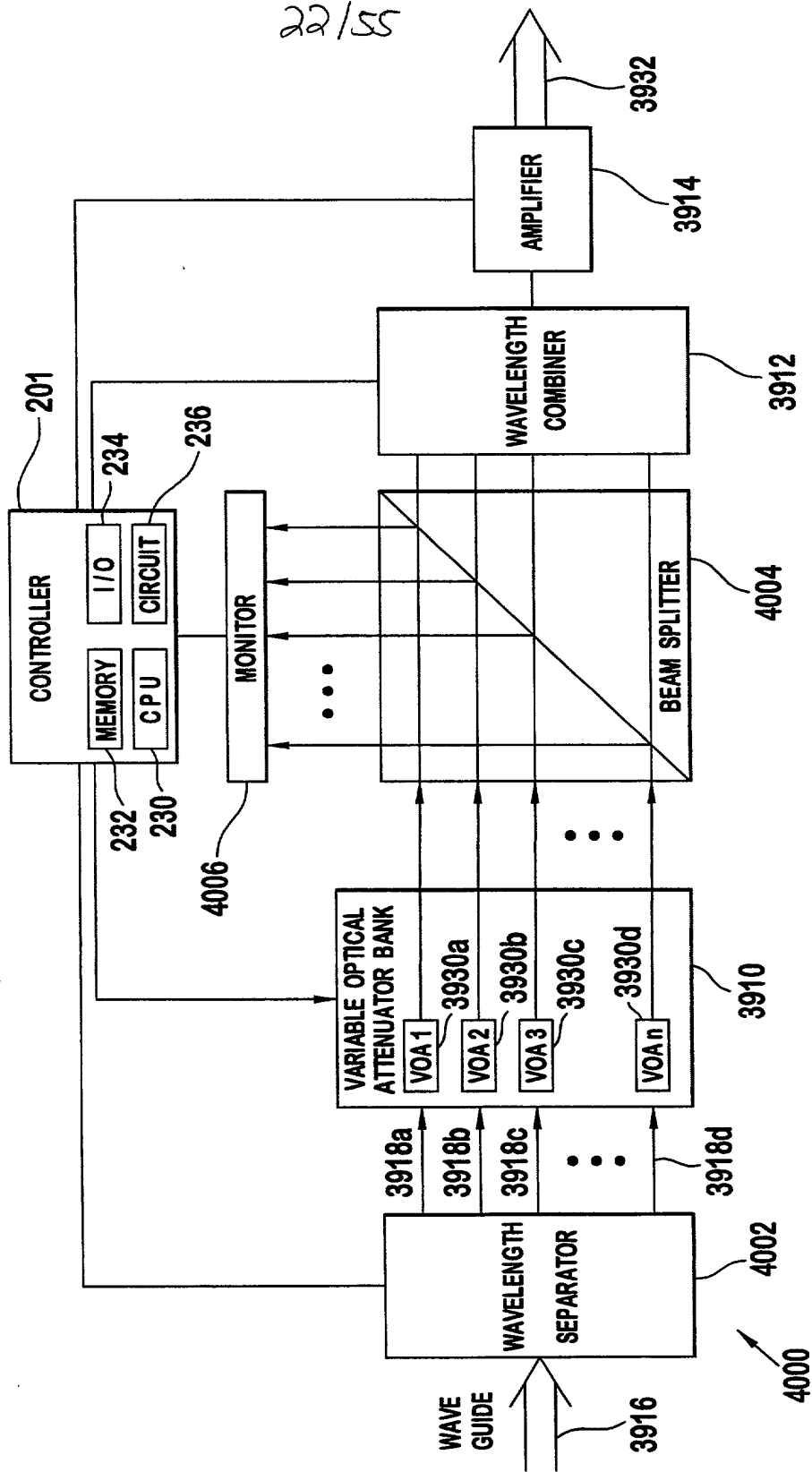
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FIG. 39



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FIG. 40



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FIG. 42

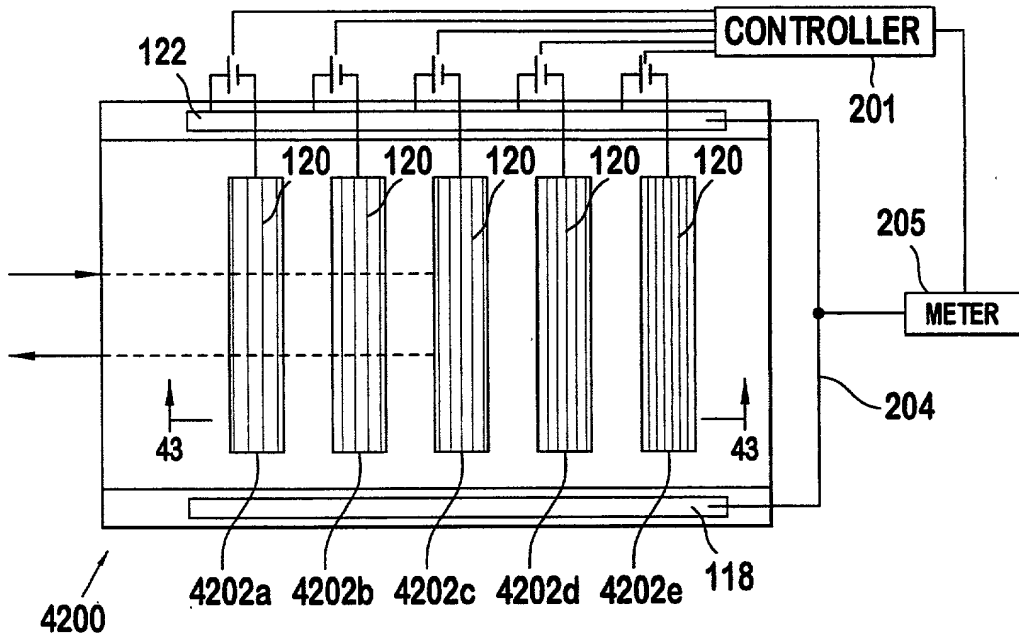
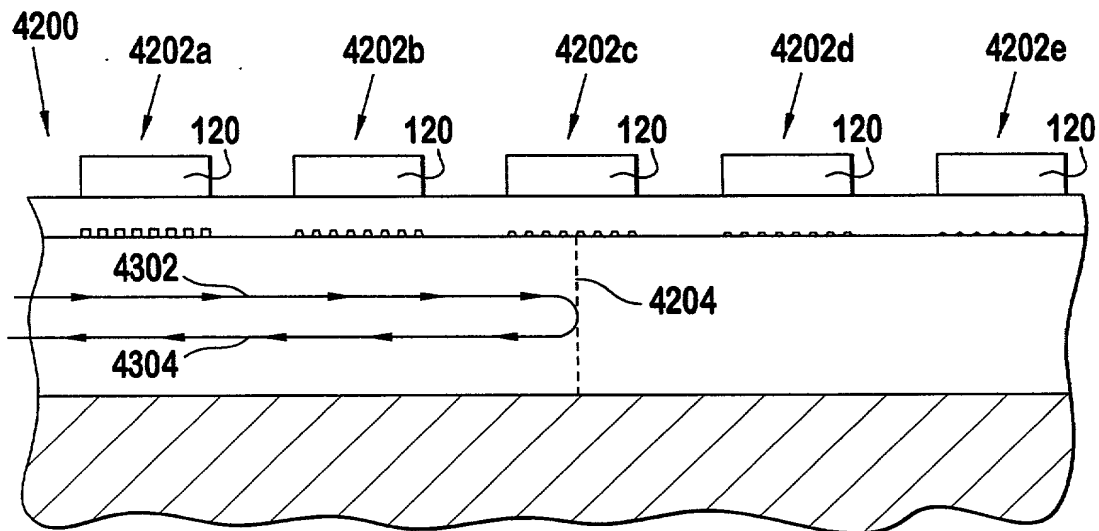


FIG. 43



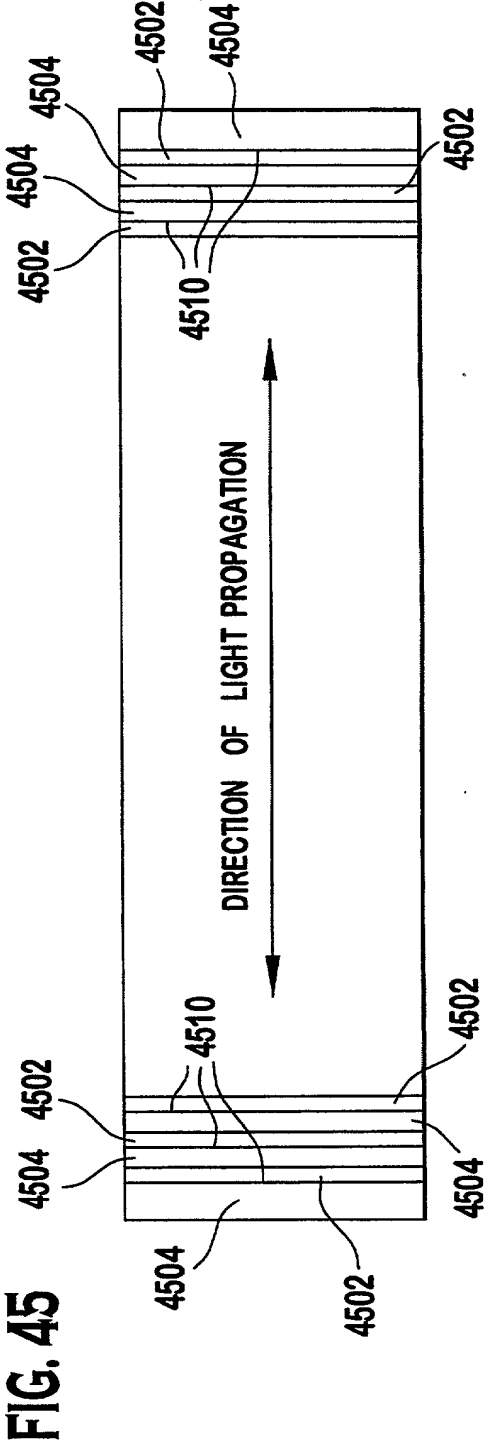
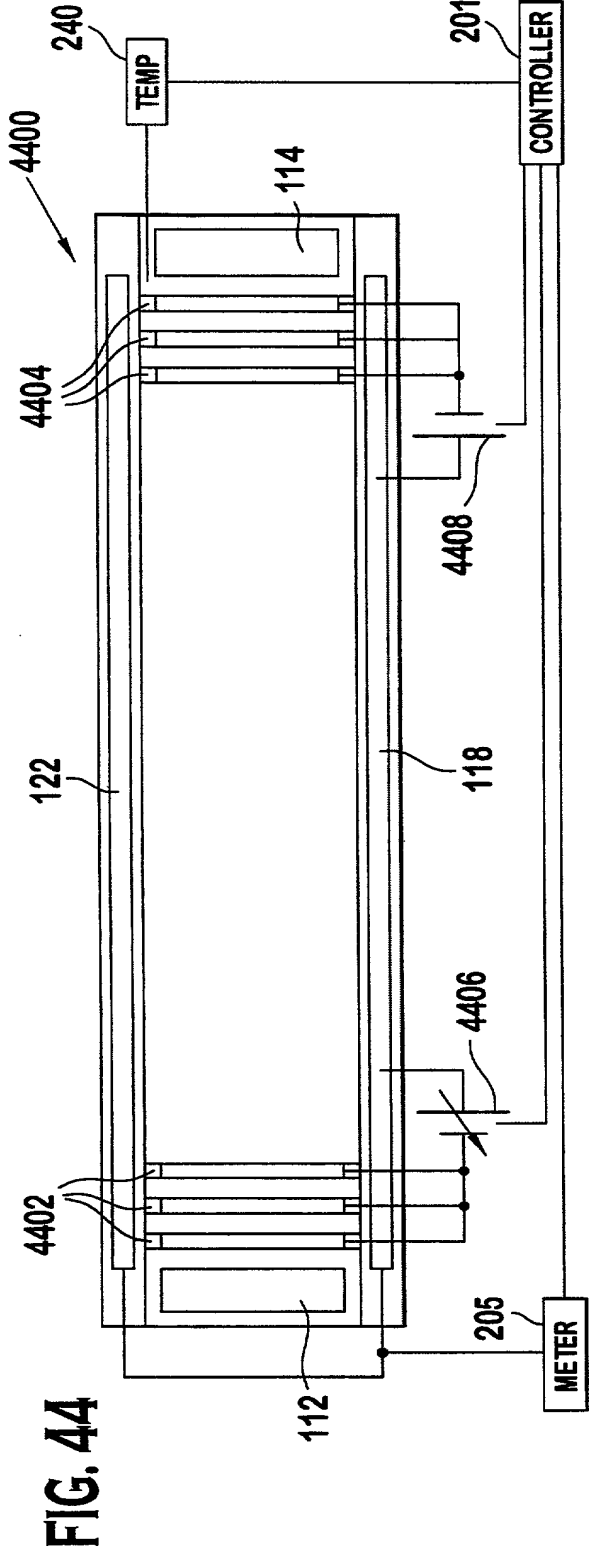


FIG. 46

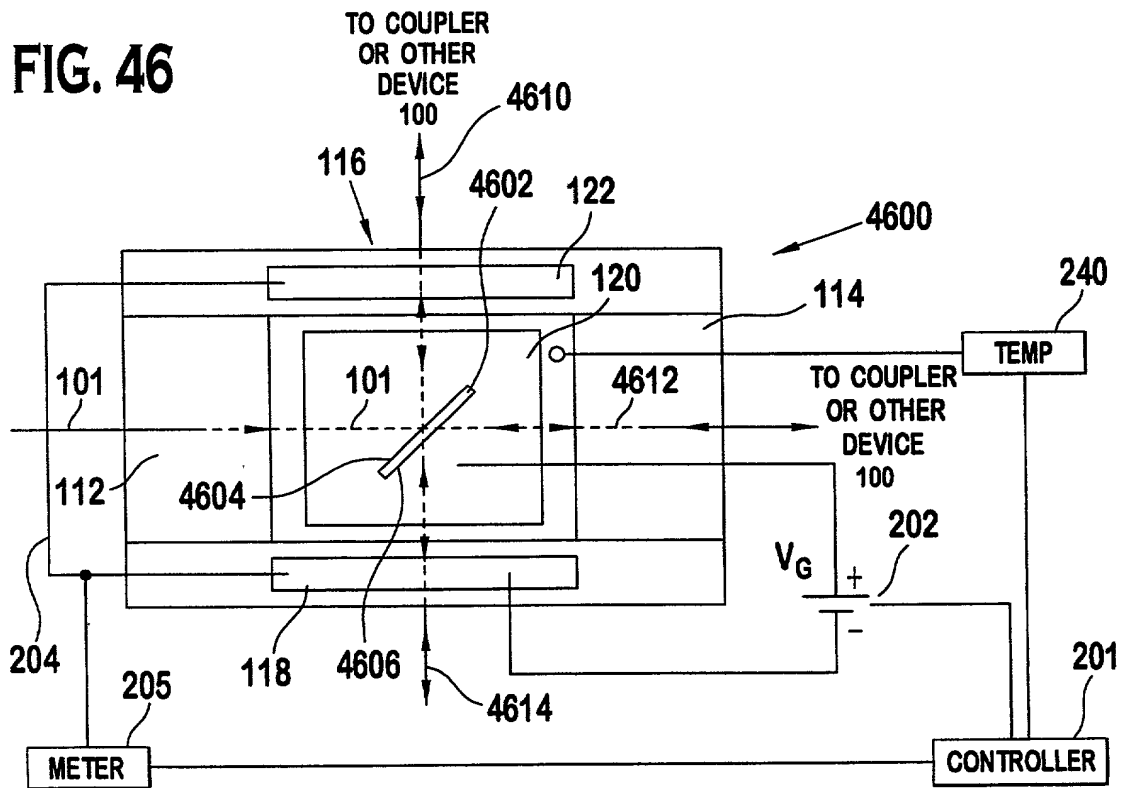
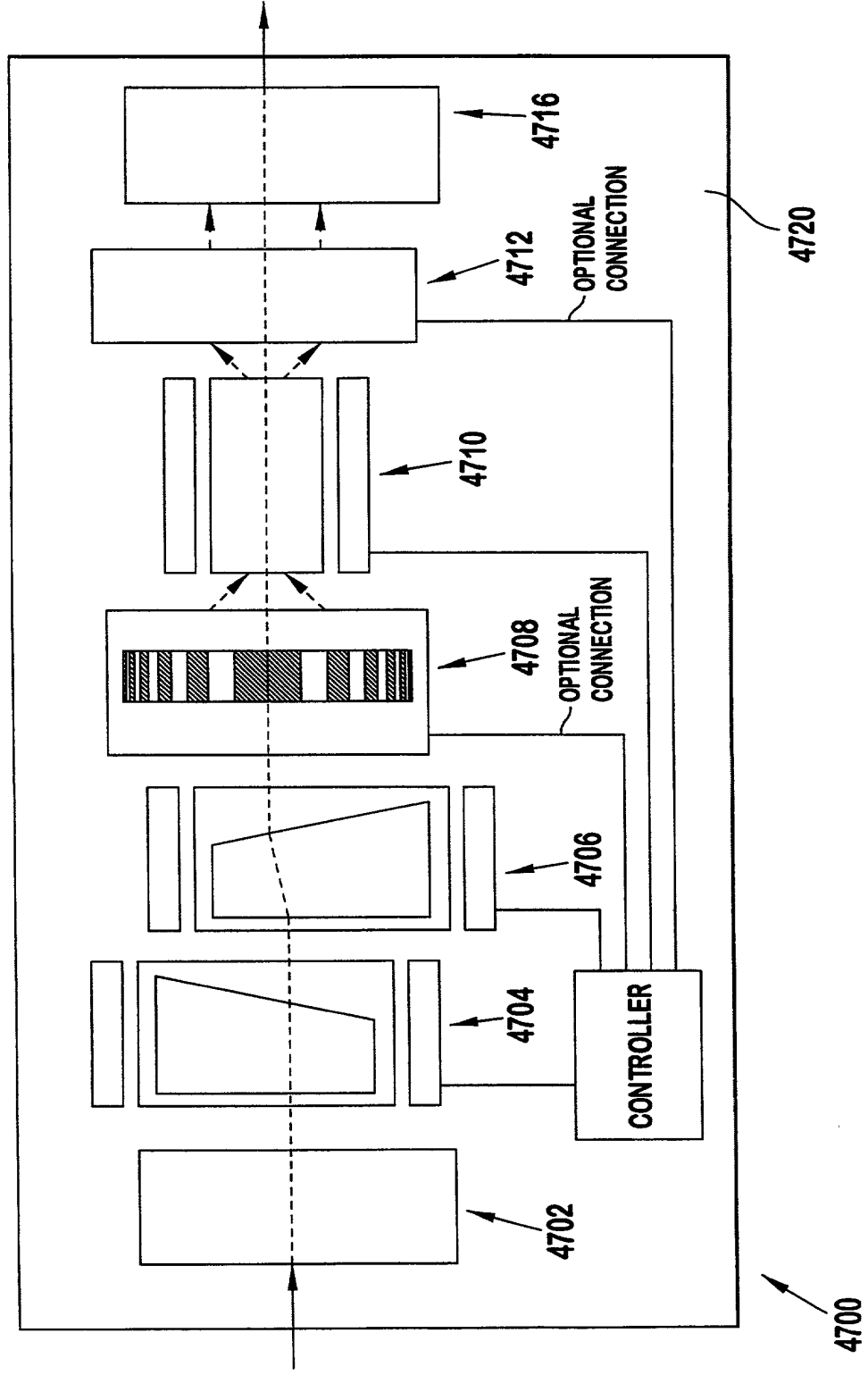


FIG. 47



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FIG. 48

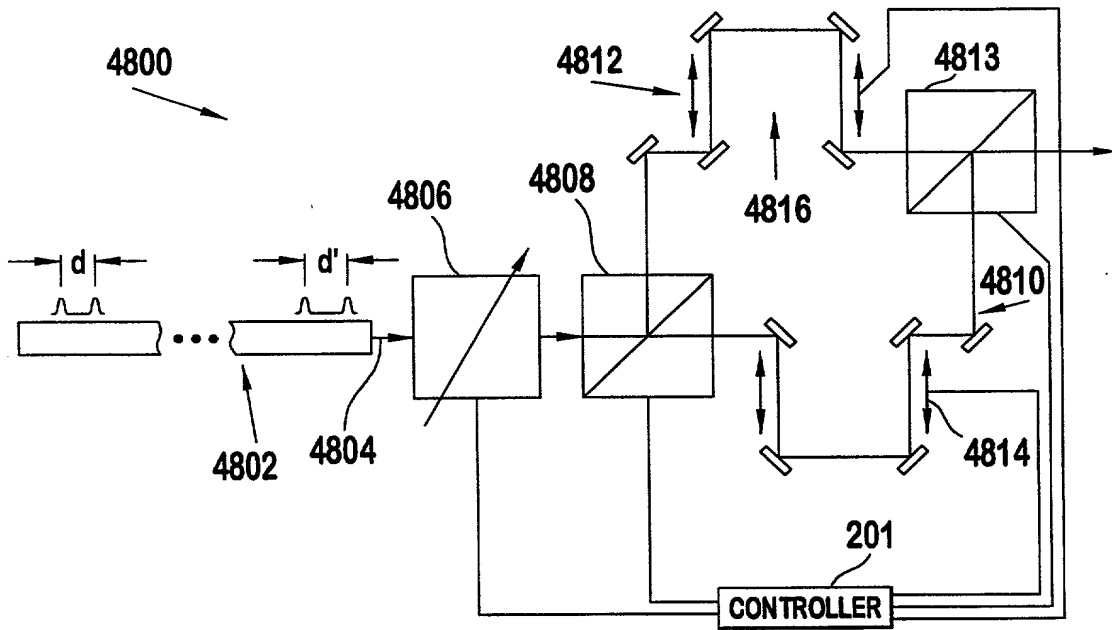
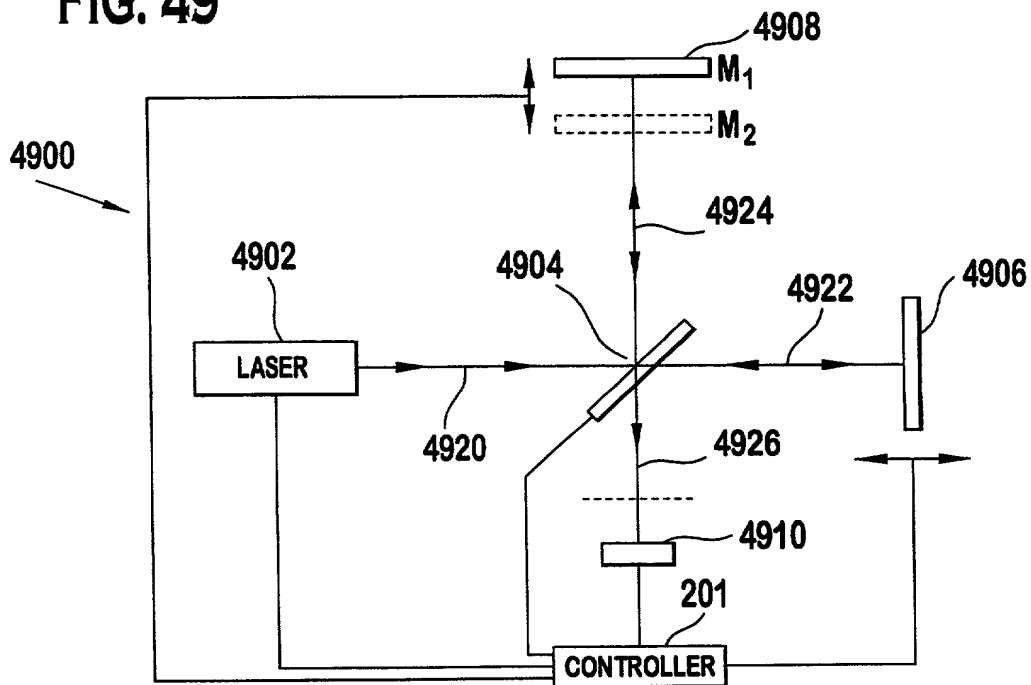
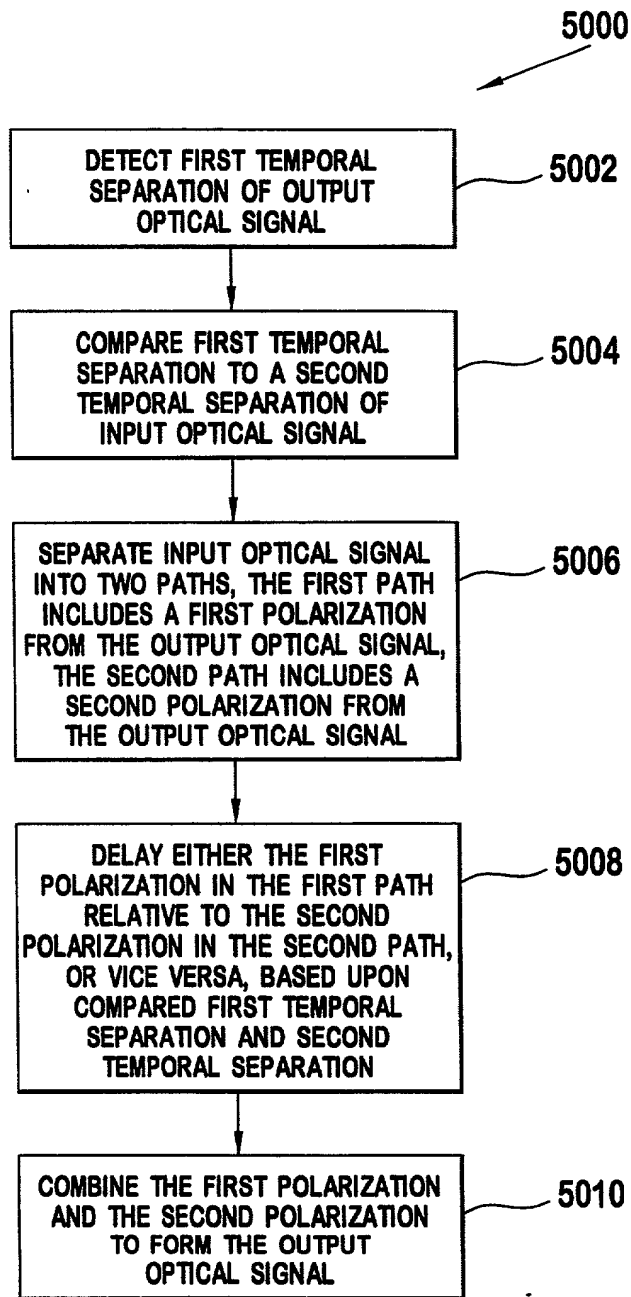


FIG. 49



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FIG. 50



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FIG. 51

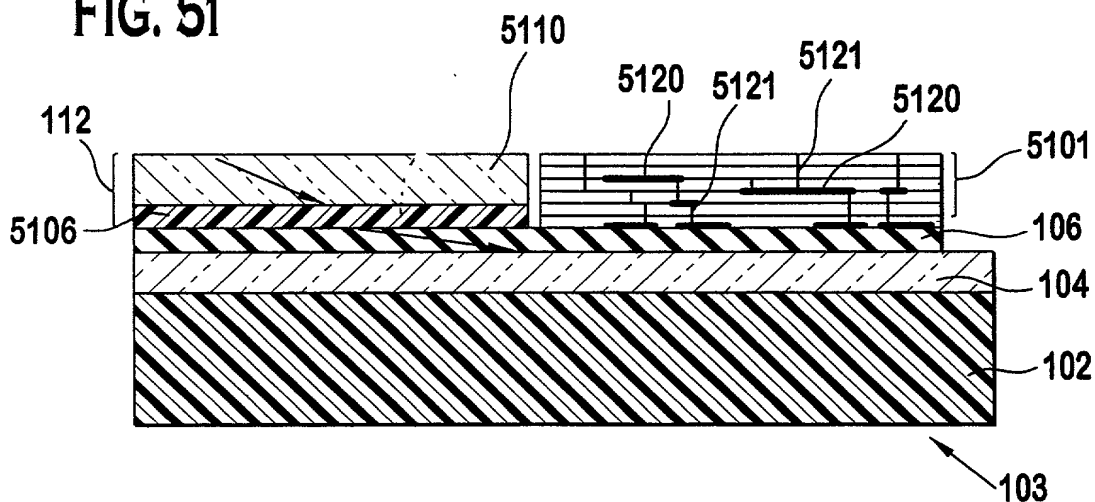
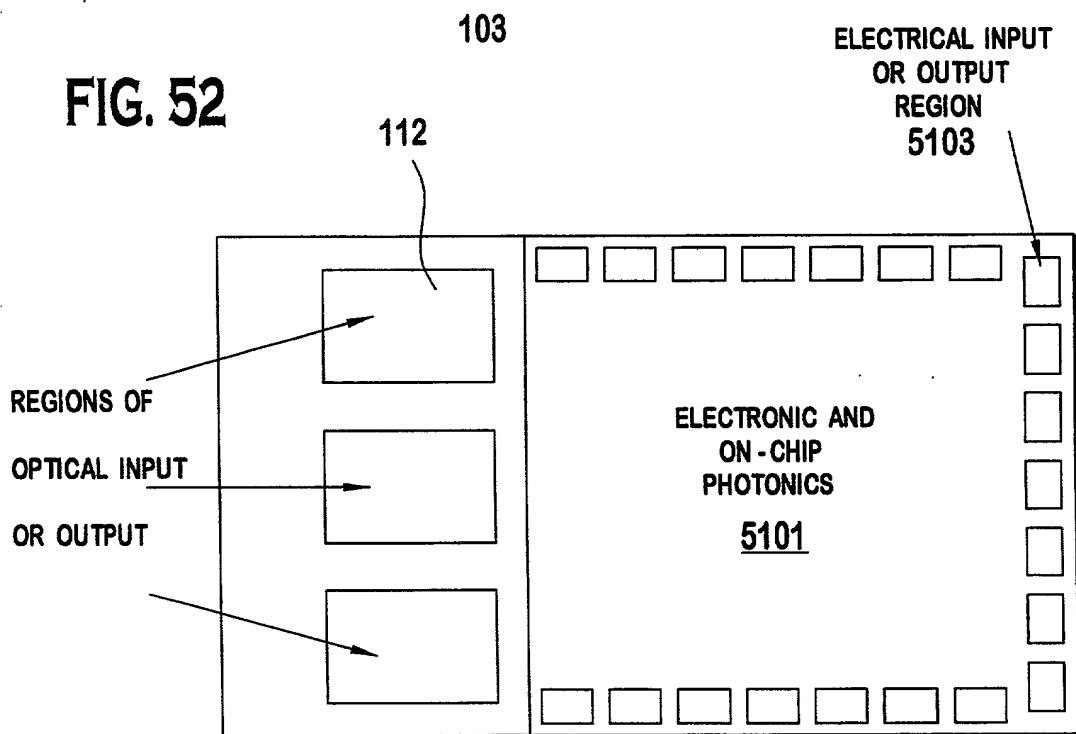


FIG. 52



[illegible]

FIG. 54

A cross-sectional view of a semiconductor device structure 112. The structure consists of several layers: a bottom substrate 102 with diagonal hatching, a layer 104 with dashed diagonal hatching, and a layer 106 with solid diagonal hatching. On top of layer 106 is a layer 5101 containing horizontal lines and vertical connections. A layer 5106 is located on the left side of the structure. A vertical wall 5110 is positioned on the right side of layer 5106. An arrow 5120 points from the left towards the structure. An arrow 112 points from the right towards the structure. An arrow 103 points from the bottom right towards the substrate 102.

FIG. 55

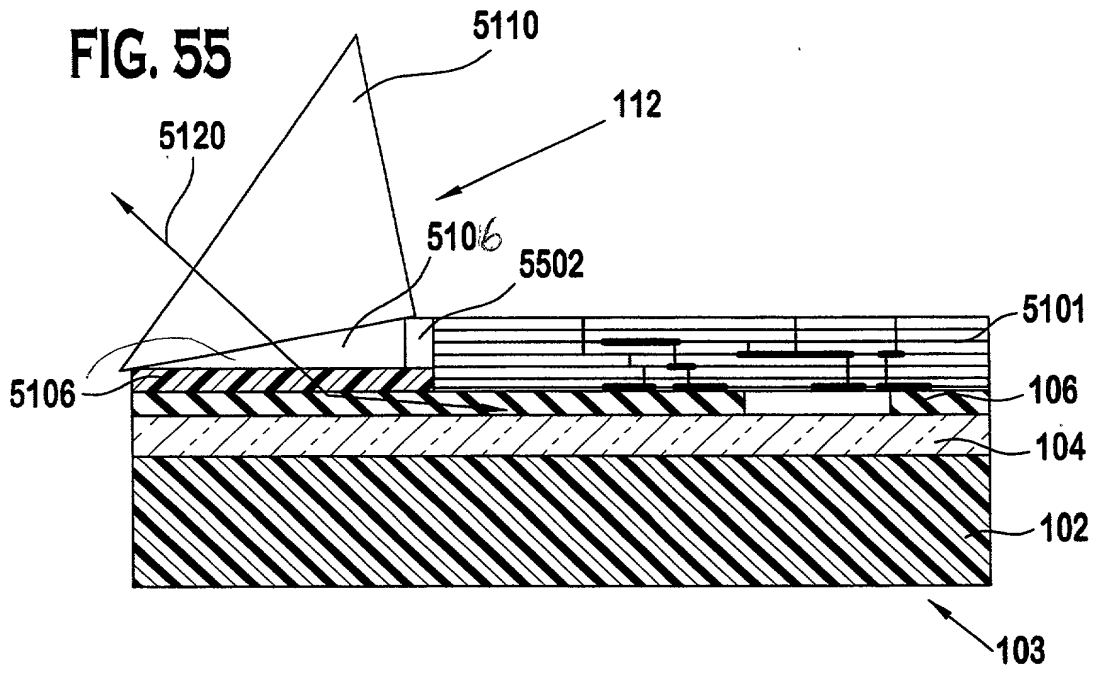


FIG. 56

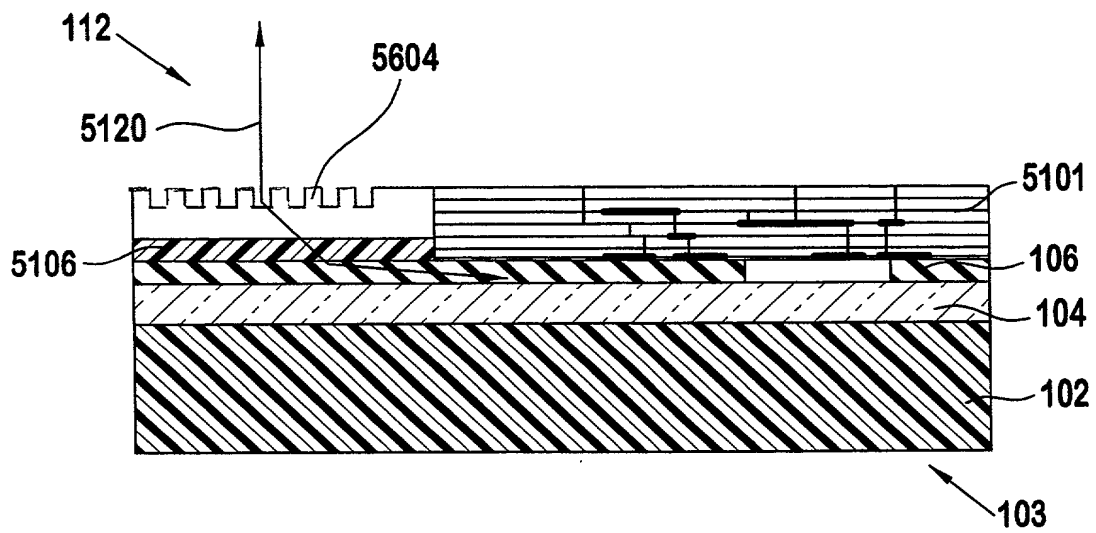


FIG. 57

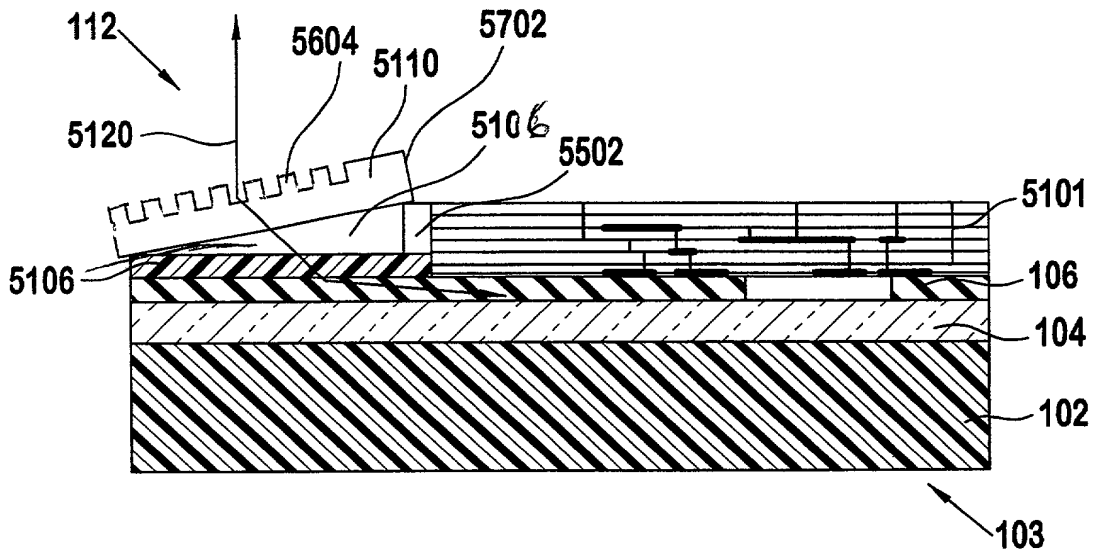
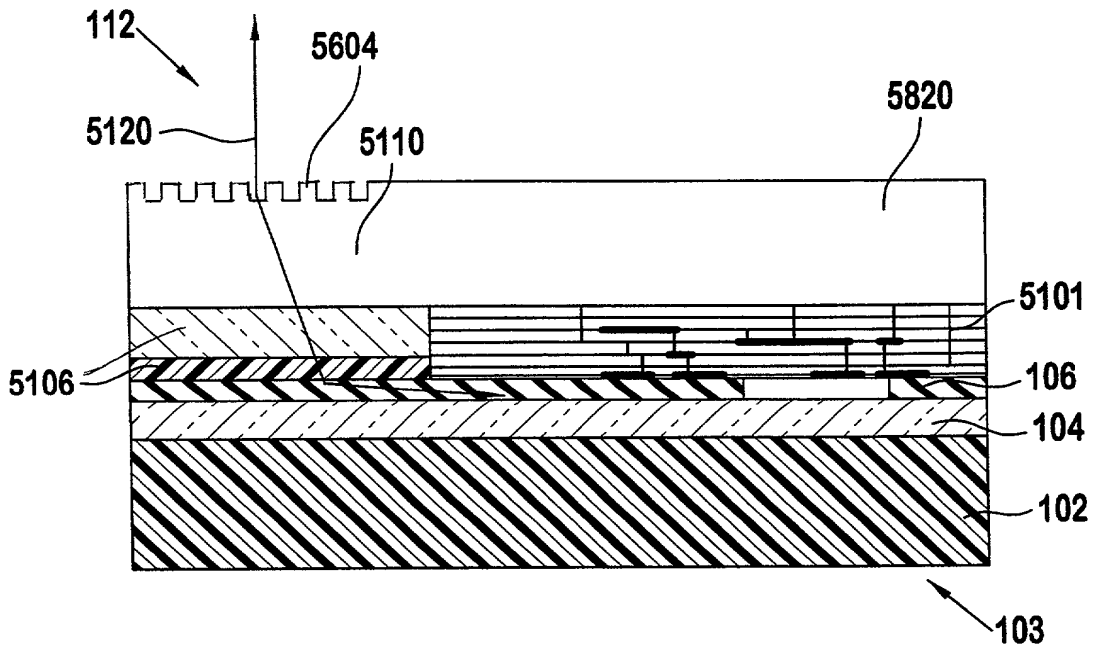


FIG. 58



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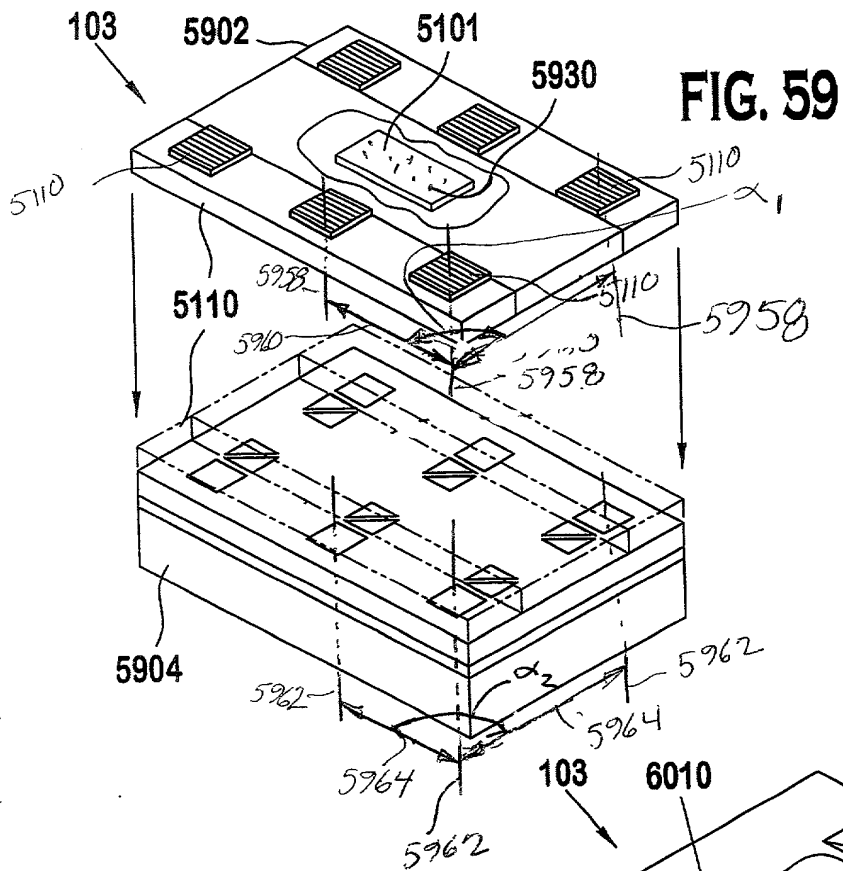


FIG. 59

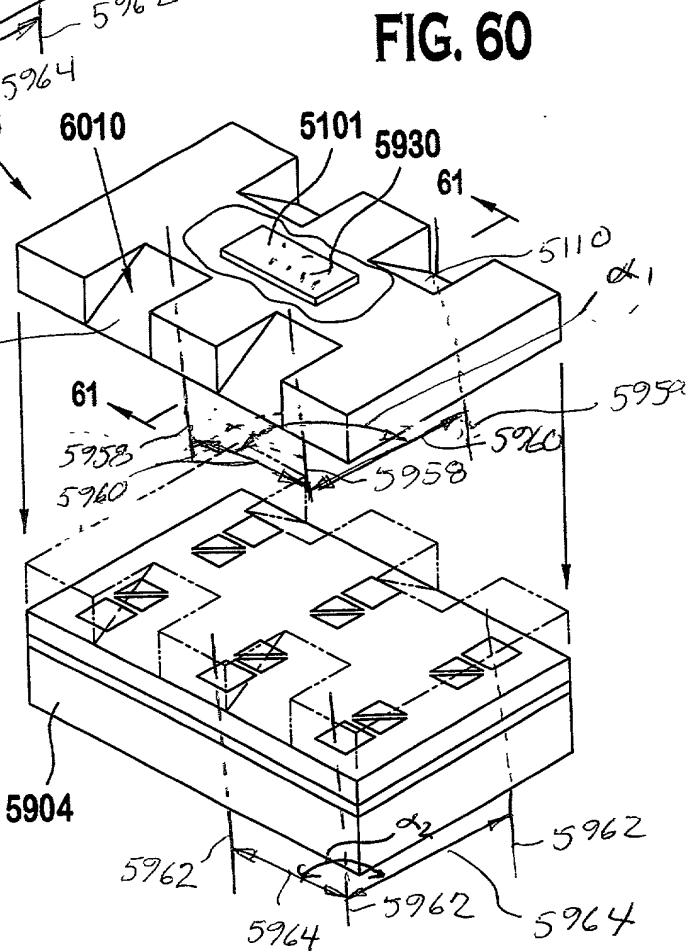


FIG. 60

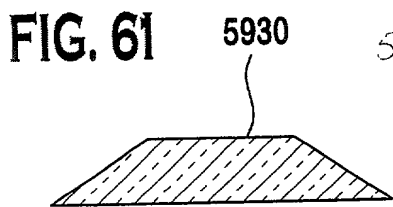


FIG. 61

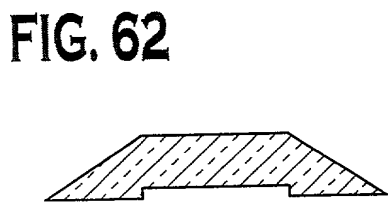


FIG. 62

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FIG. 63A

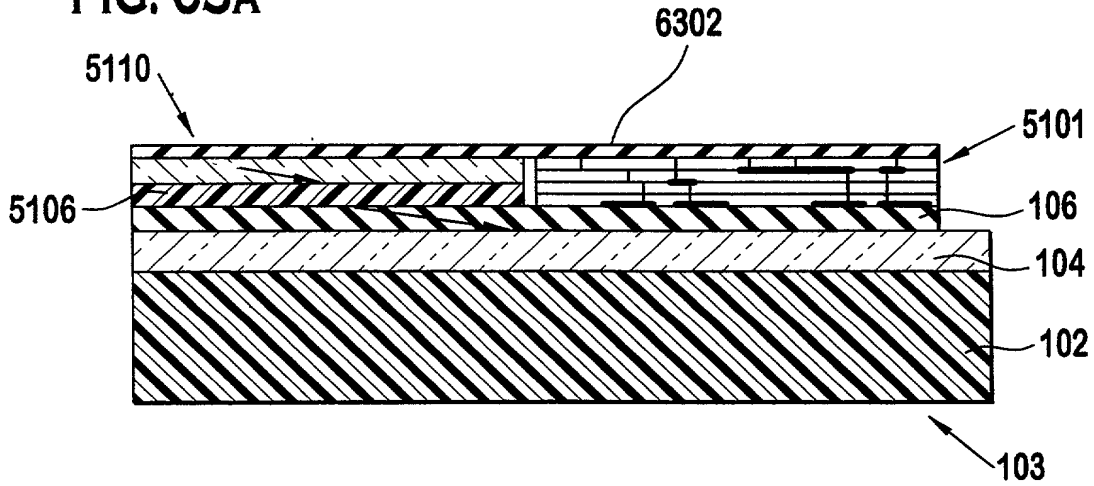


FIG. 63B

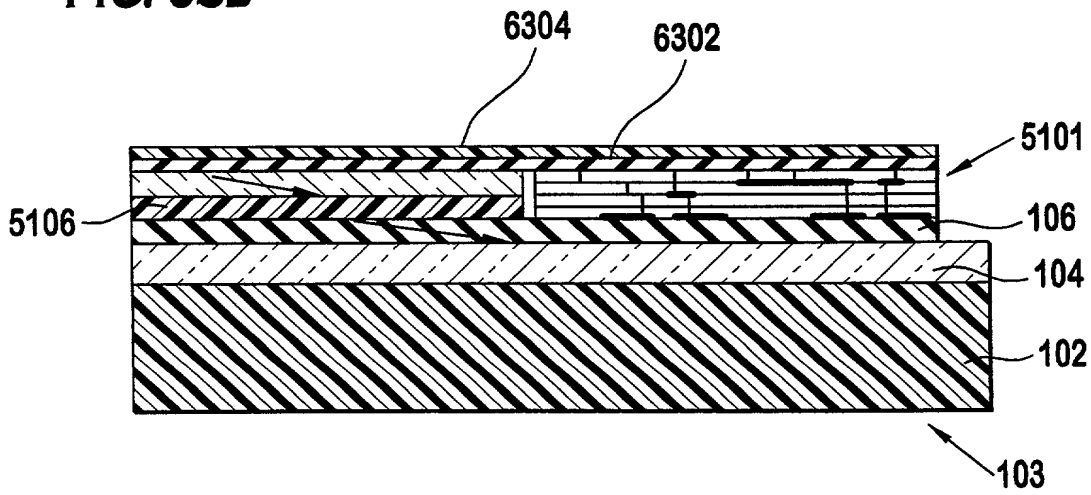


FIG. 63c

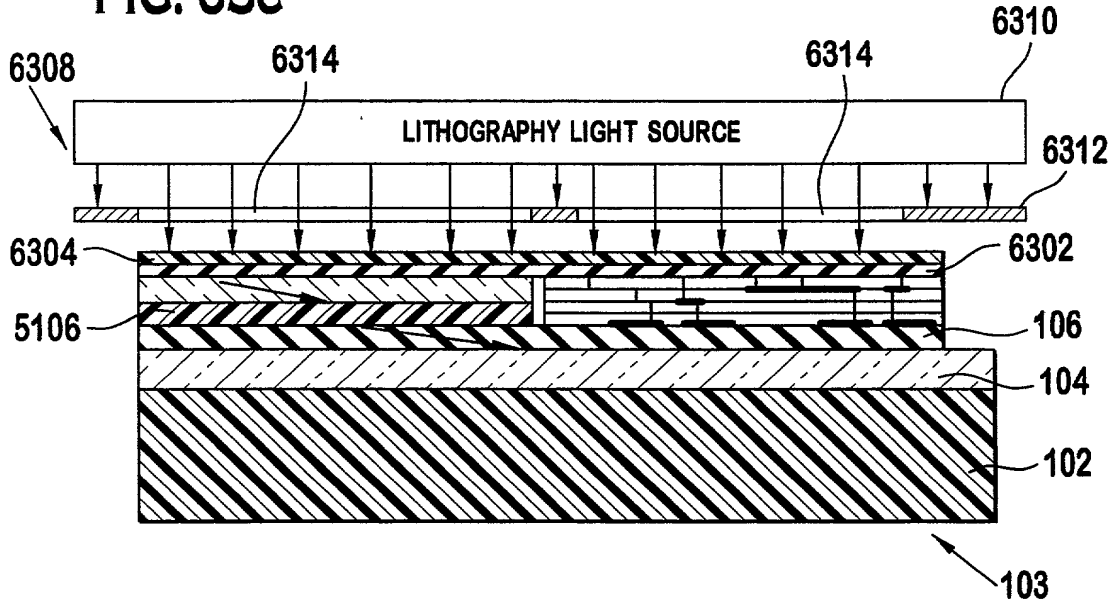
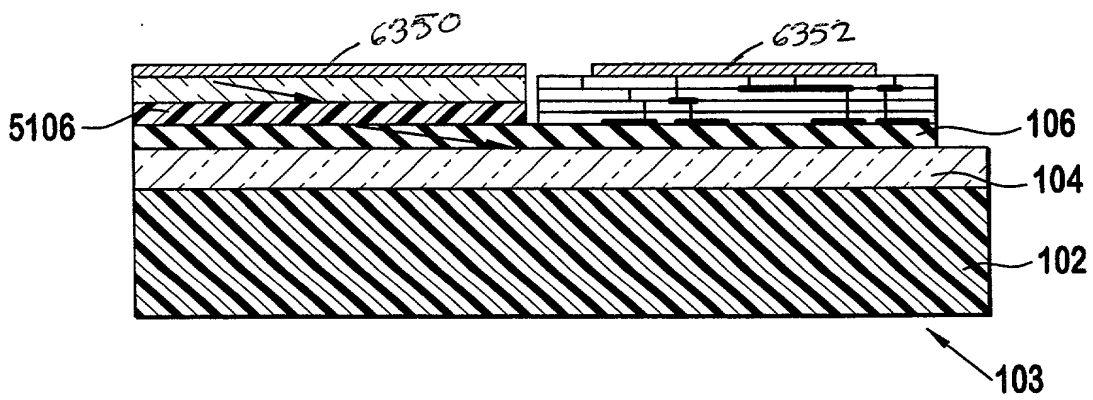


FIG. 63d



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FIG. 64

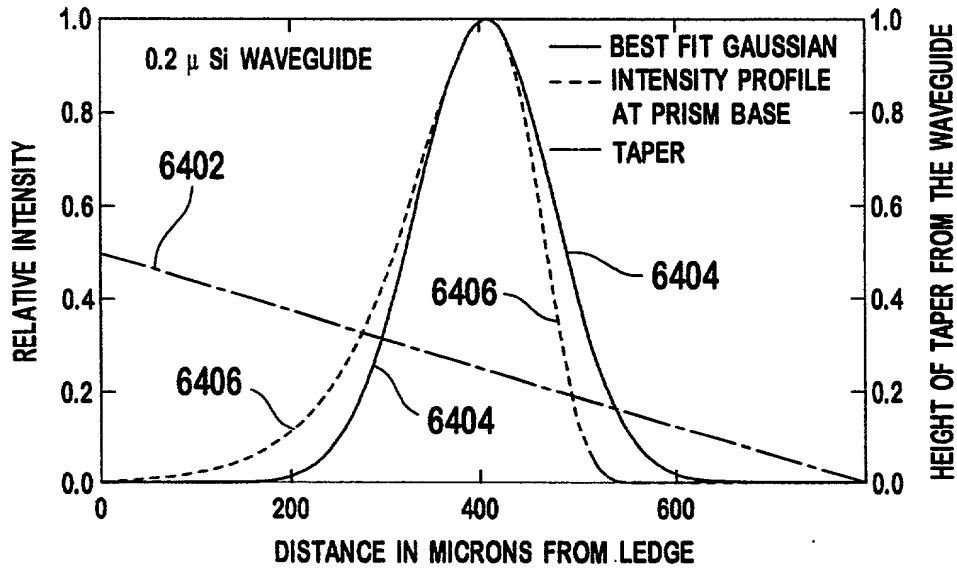
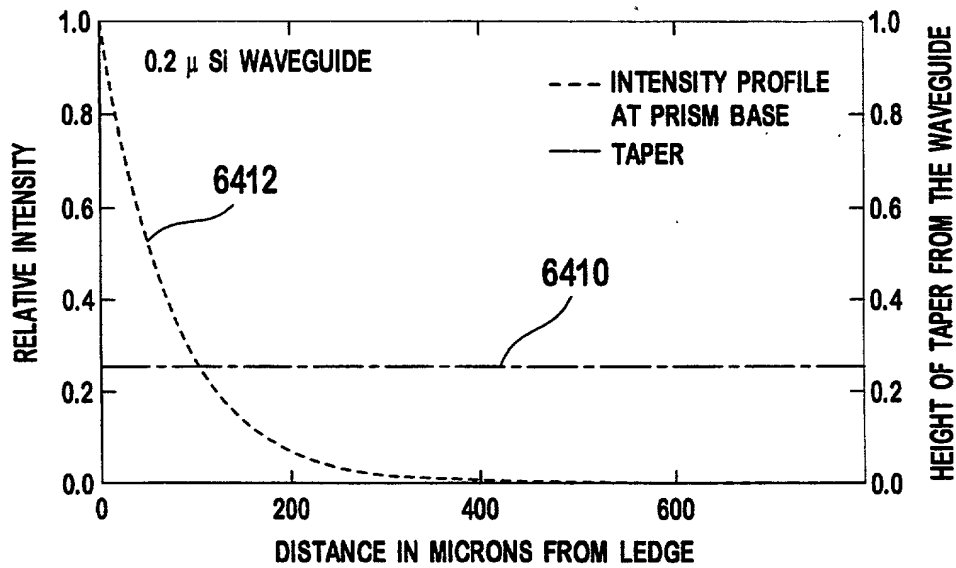


FIG. 65



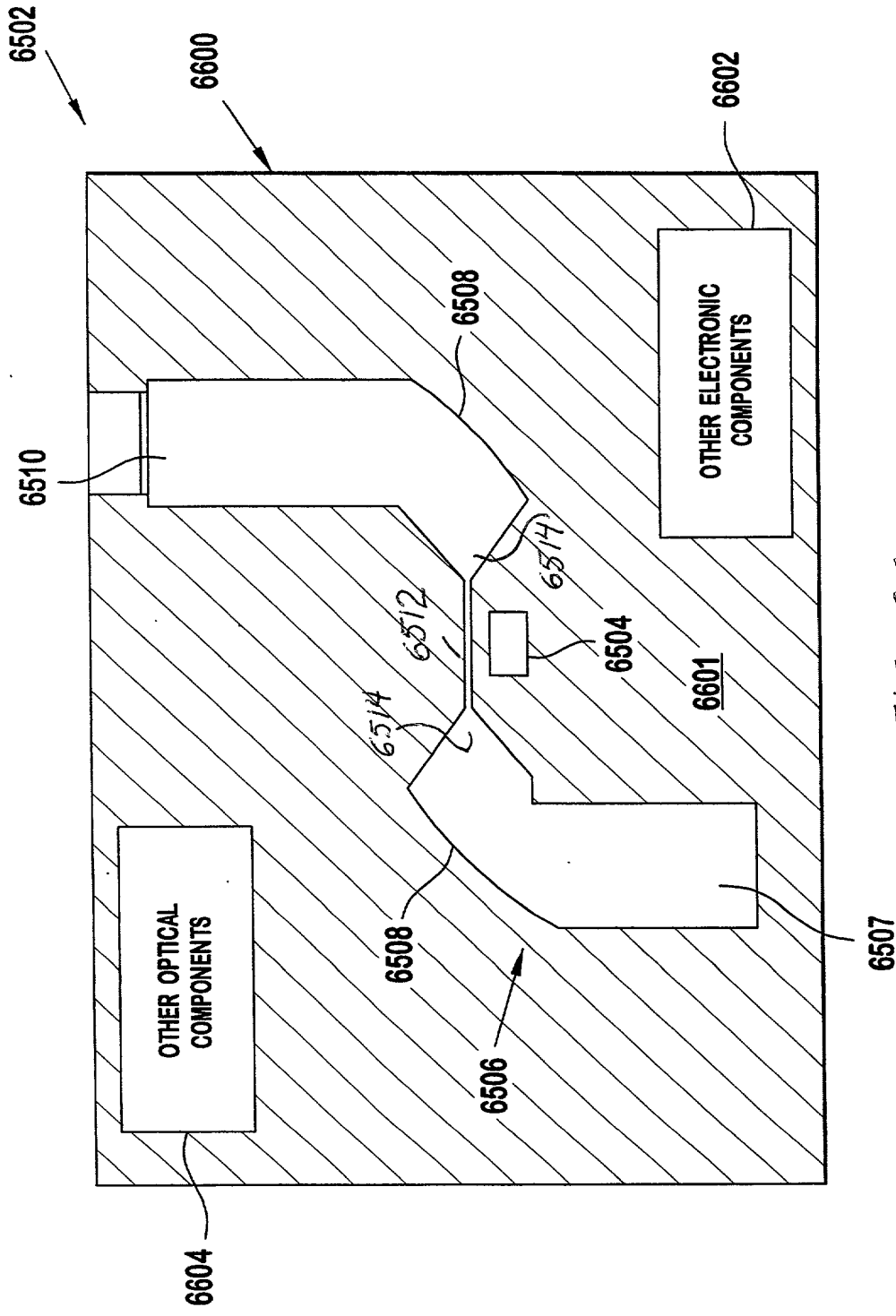


FIG. 66

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FIG. 67

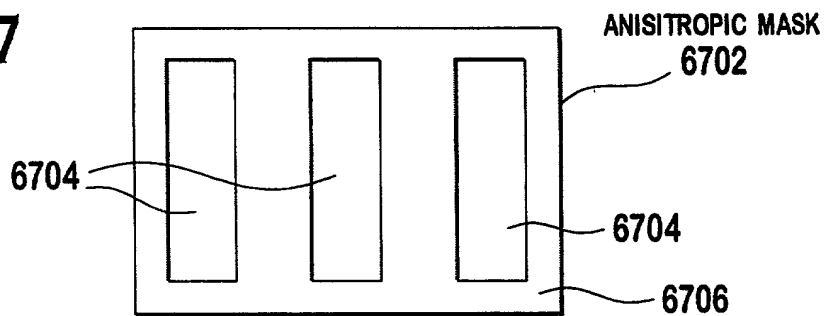


FIG. 68A



FIG. 68B

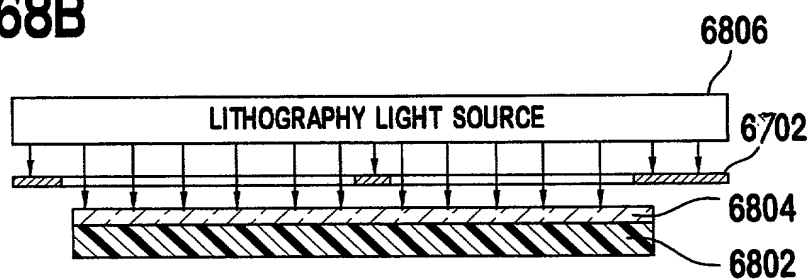
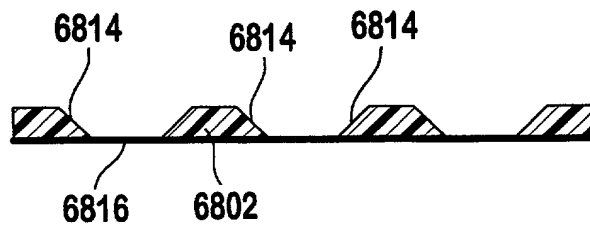


FIG. 68C



FIG. 68D



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FIG. 69

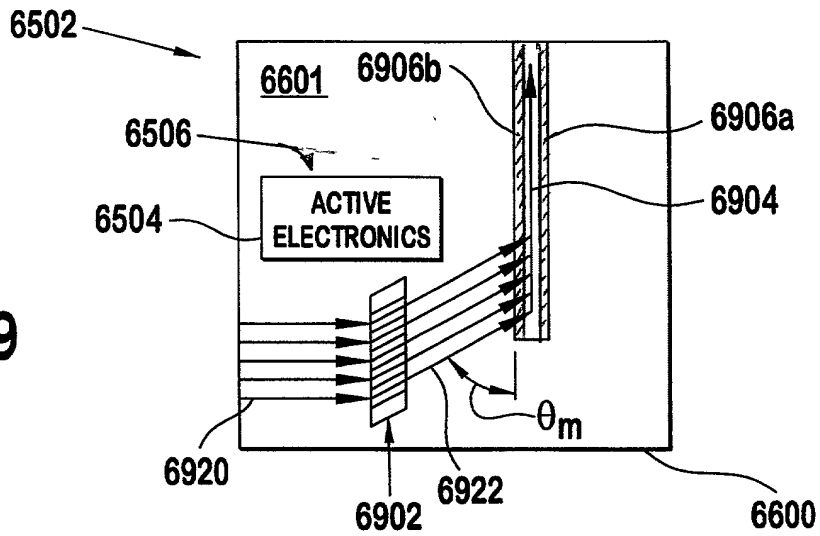


FIG. 70

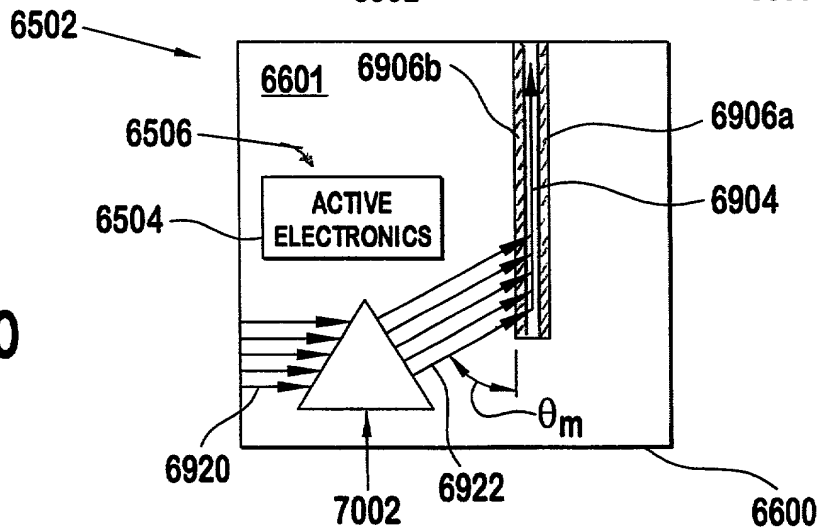
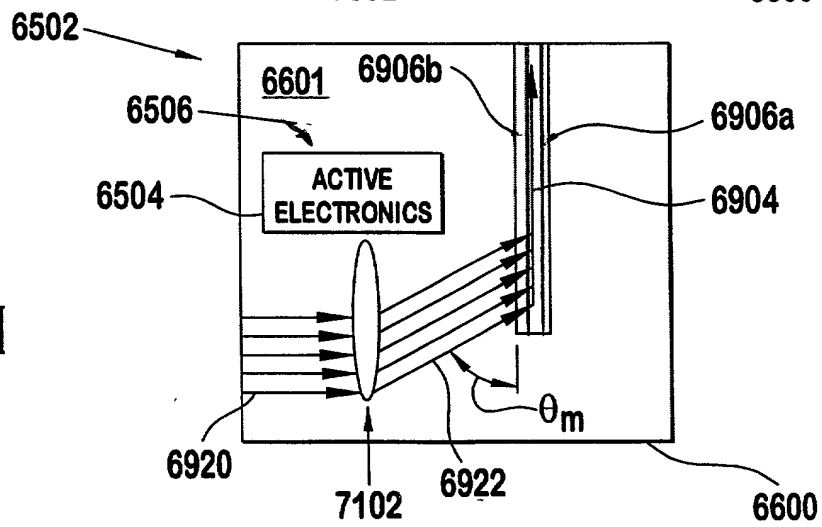


FIG. 71



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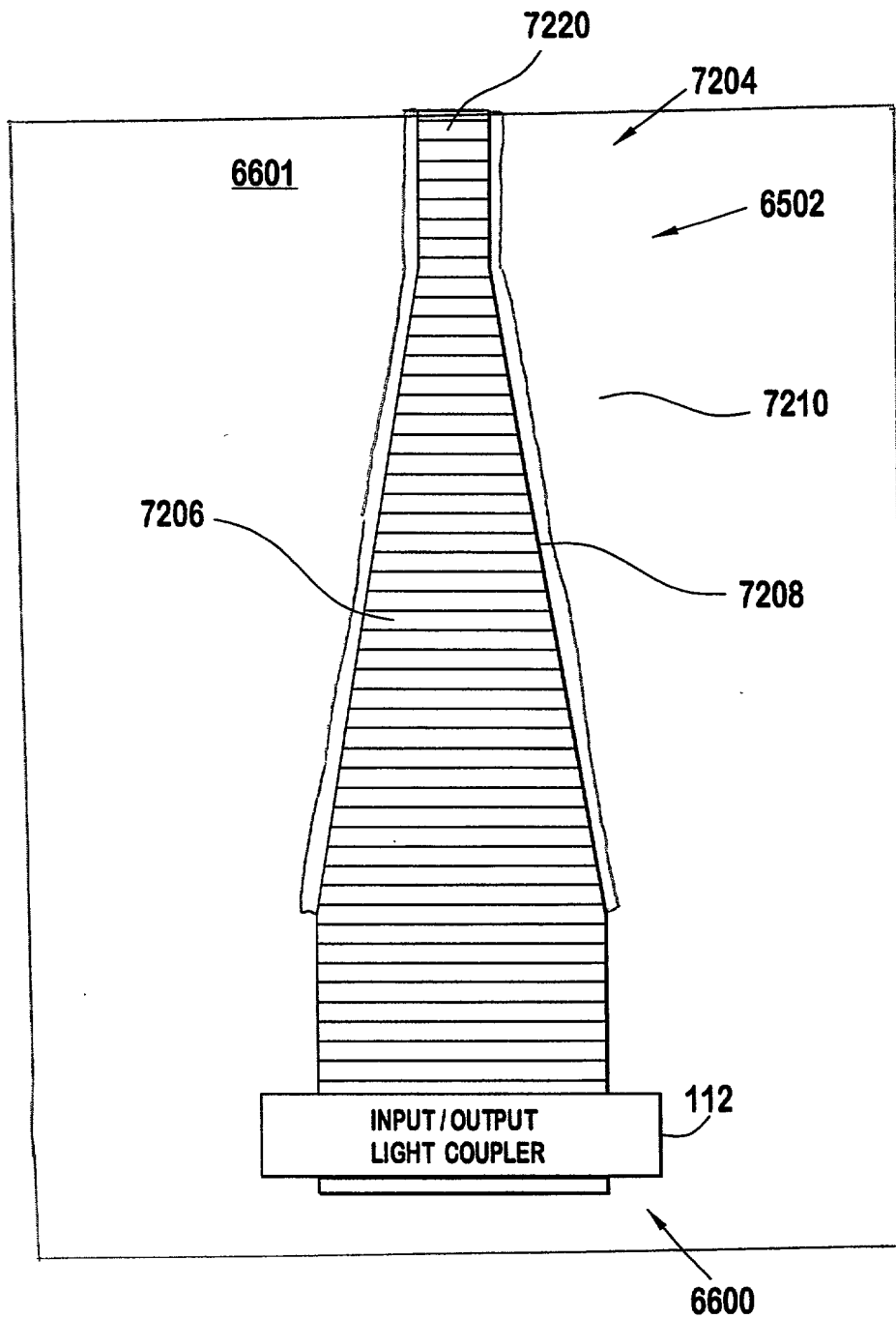


FIG. 72

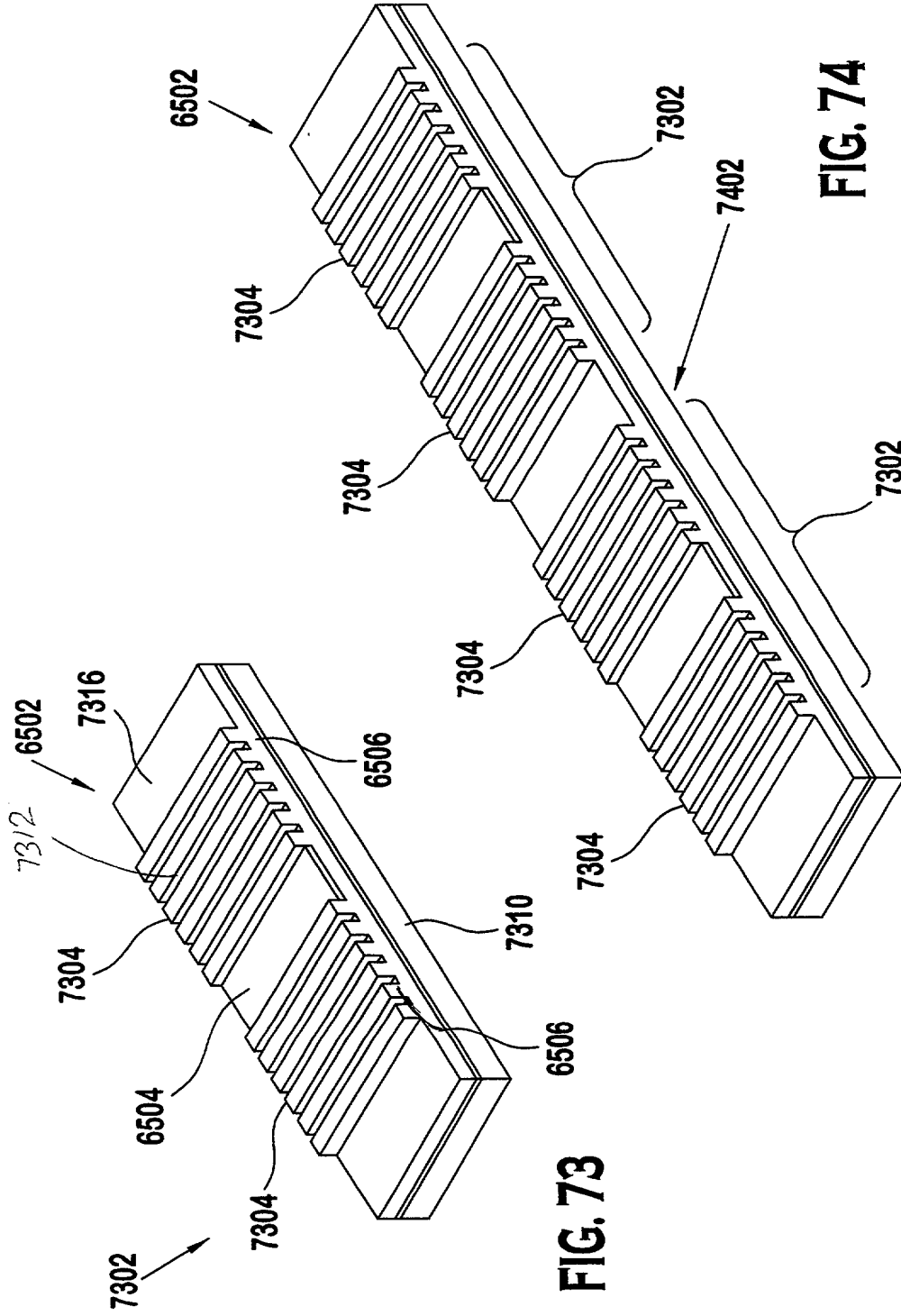


FIG. 75

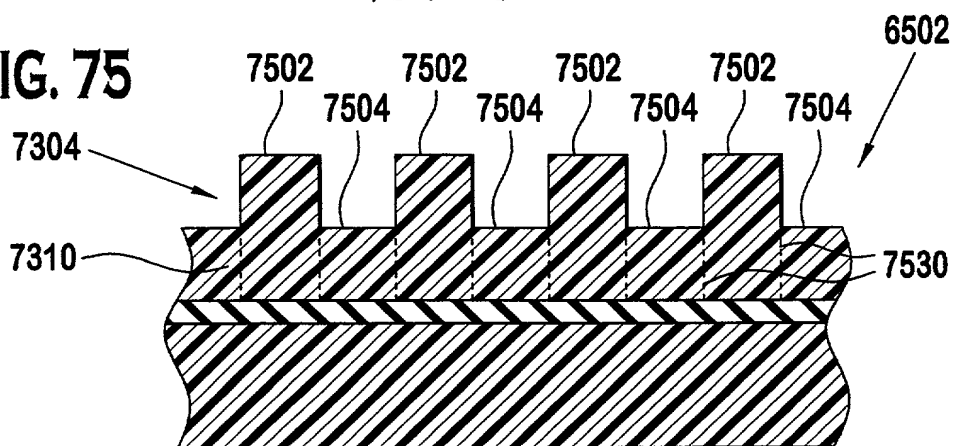


FIG. 76

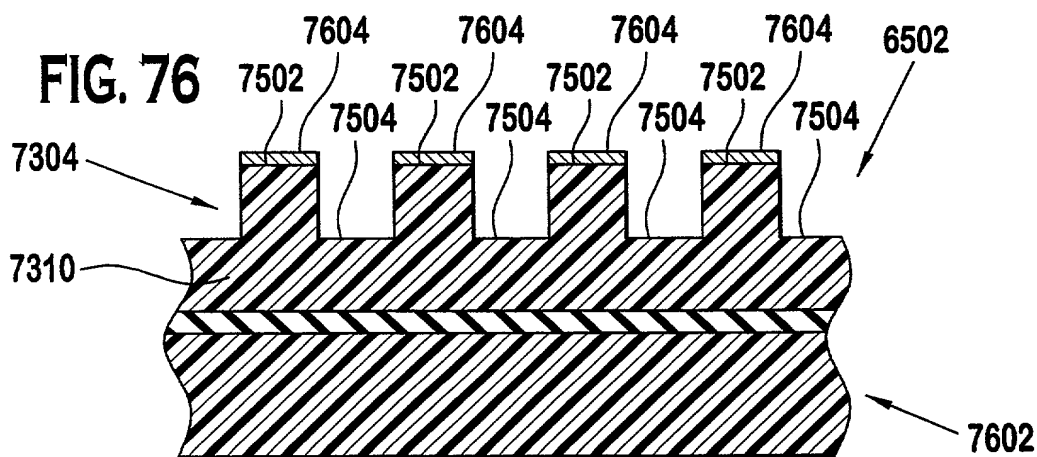
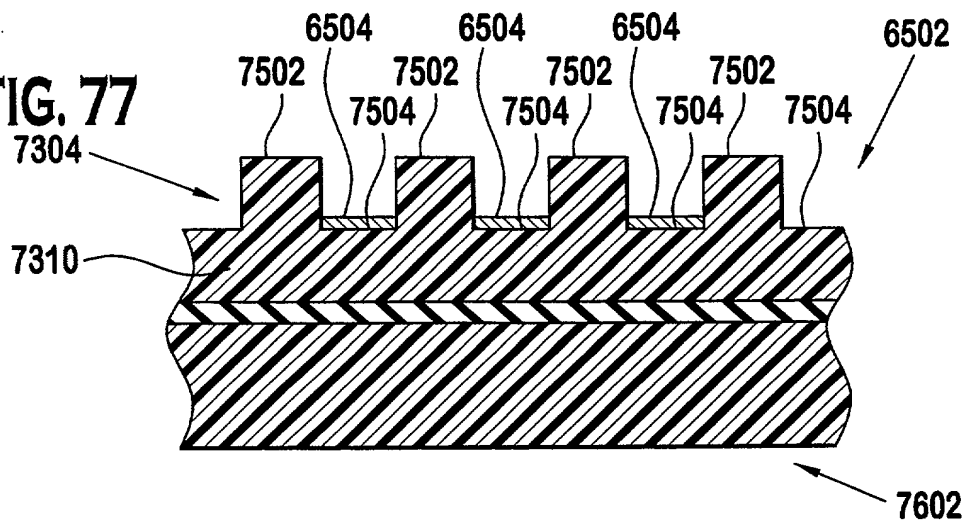


FIG. 77



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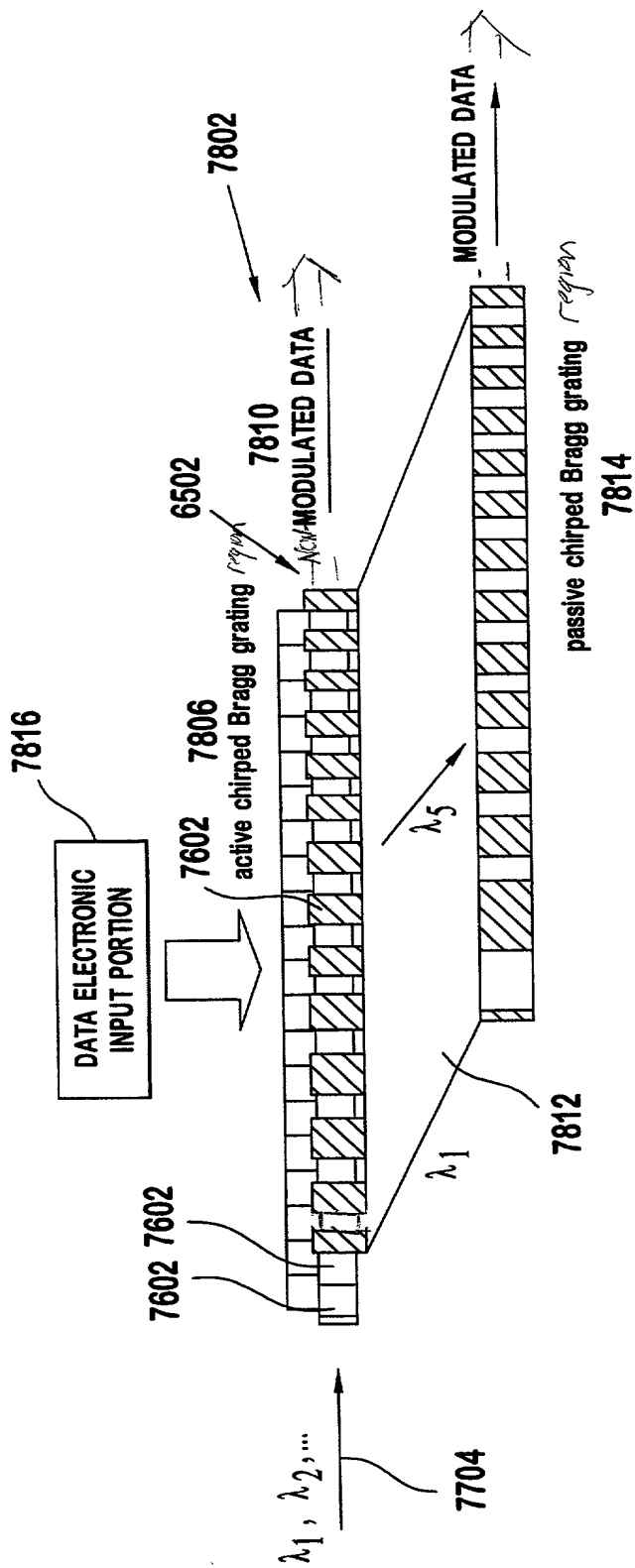


FIG. 78

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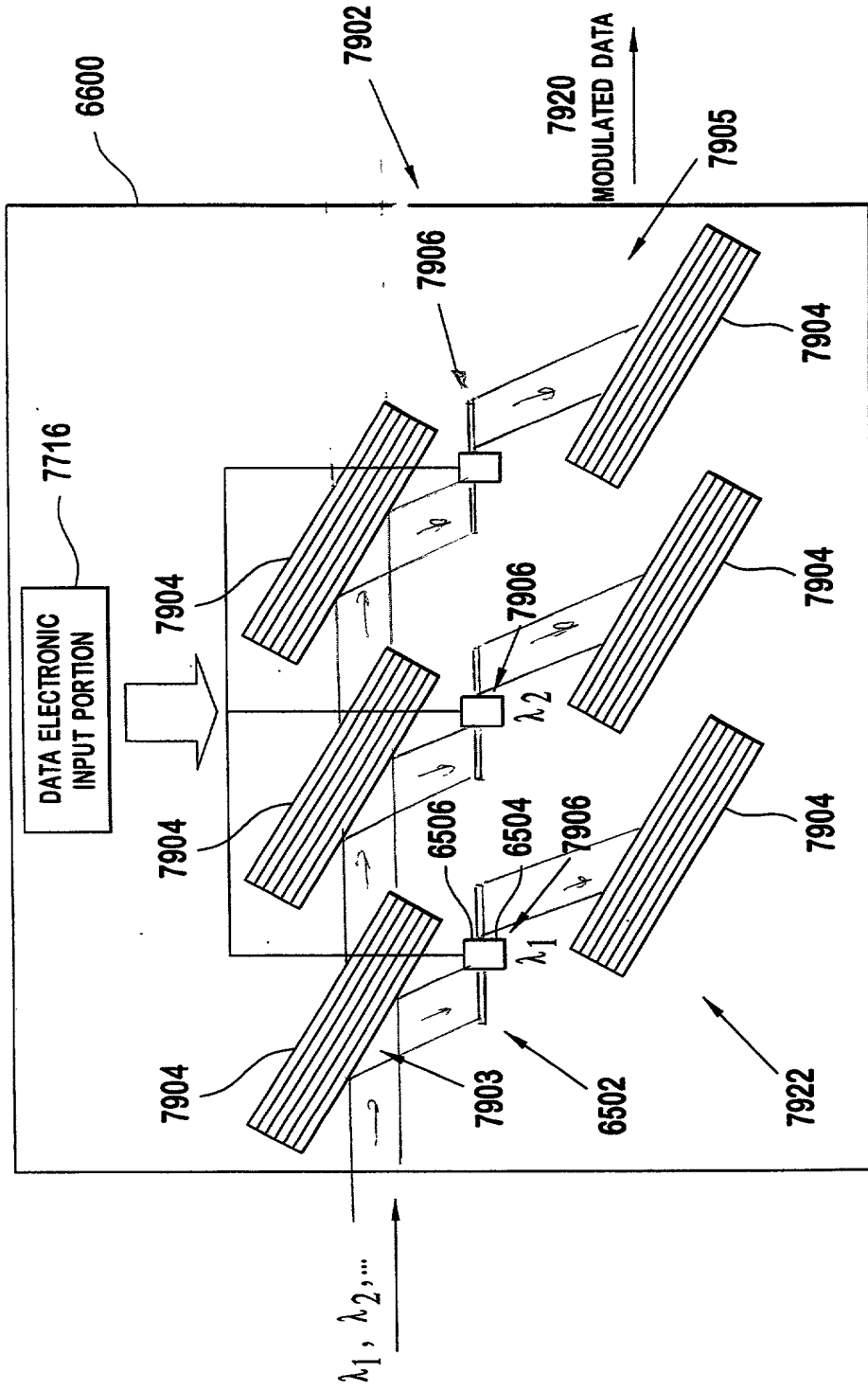


FIG. 79

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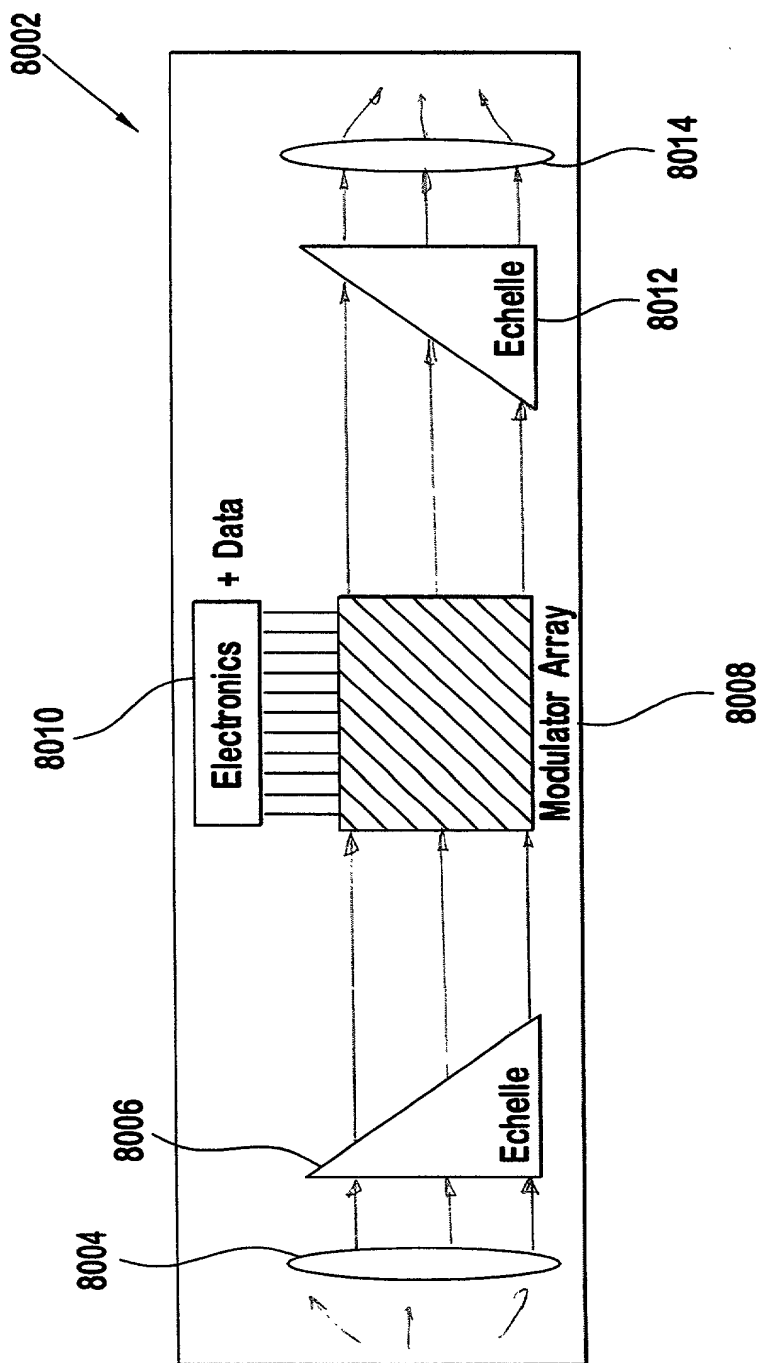


FIG. 80

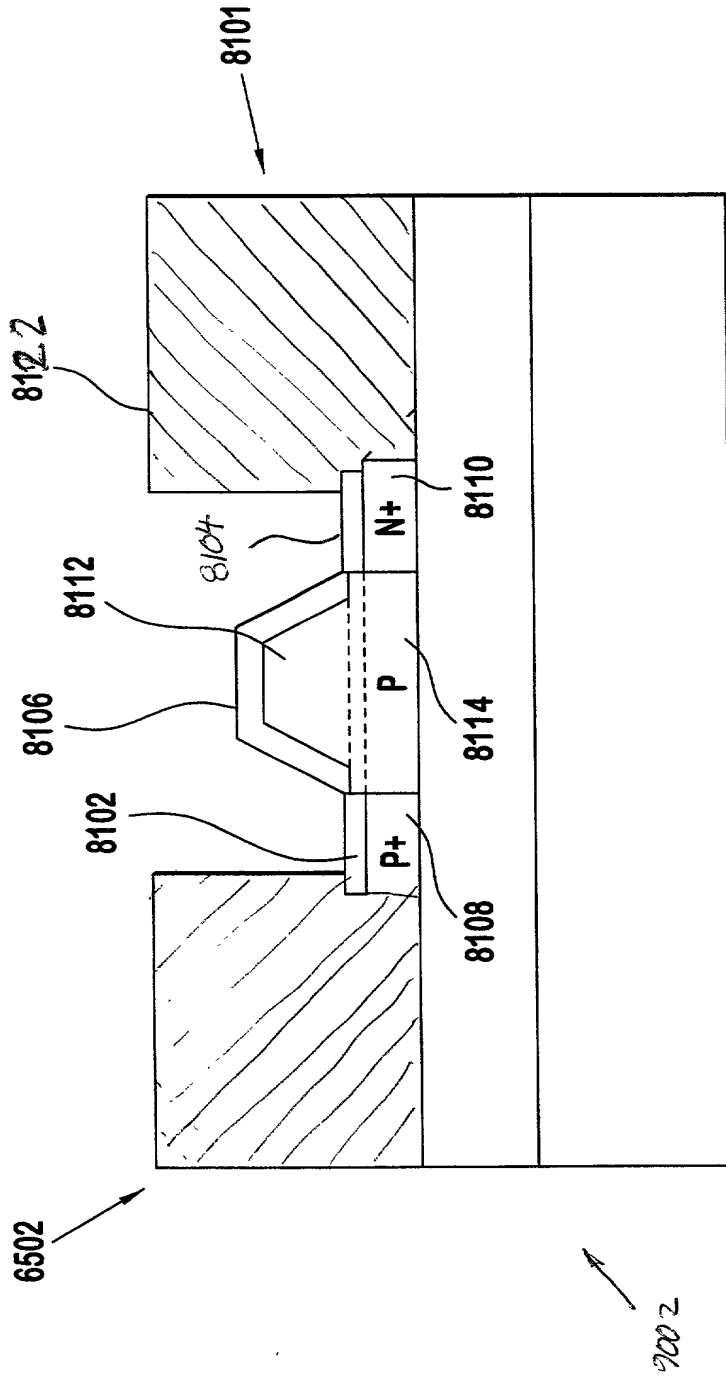


FIG. 81

FIG. 82

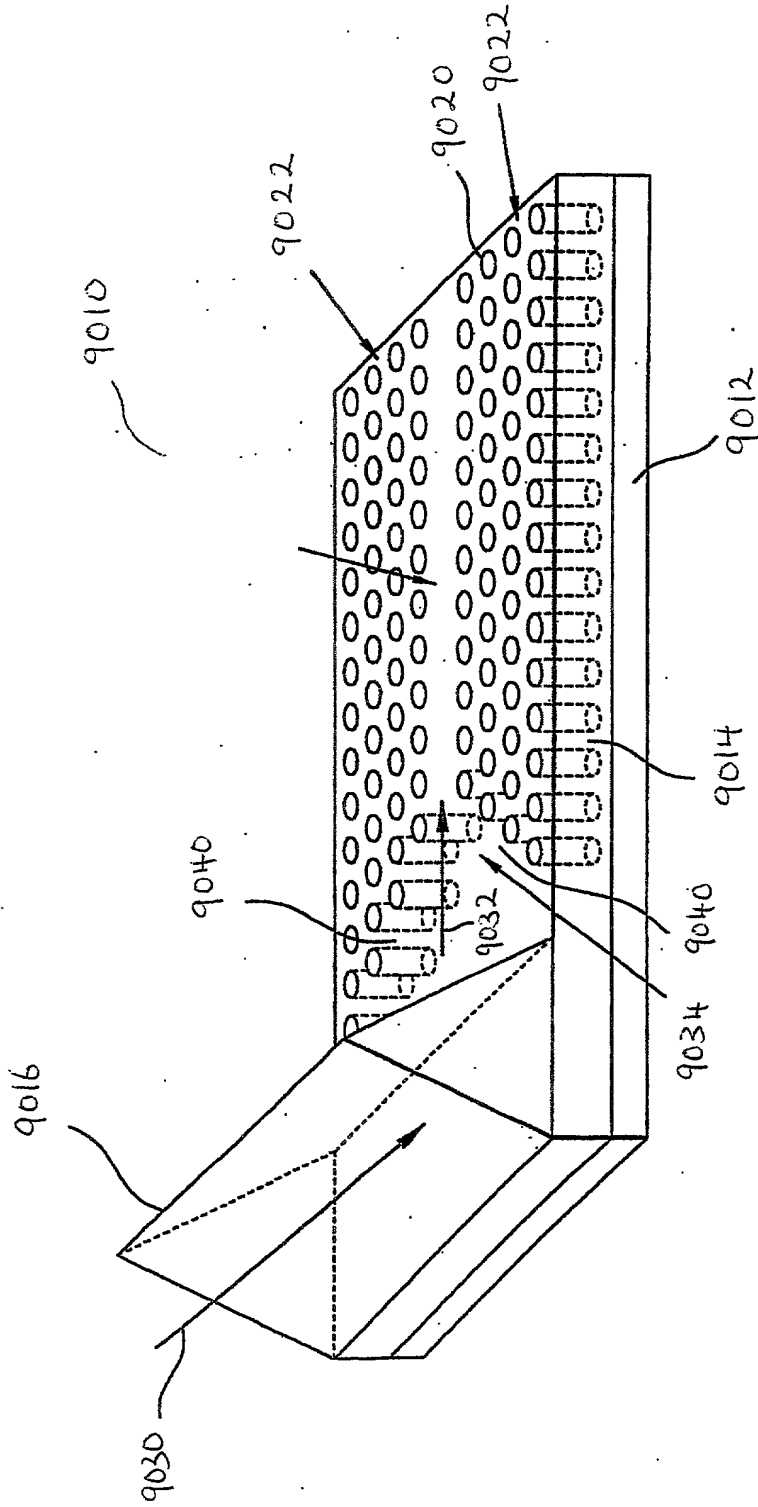
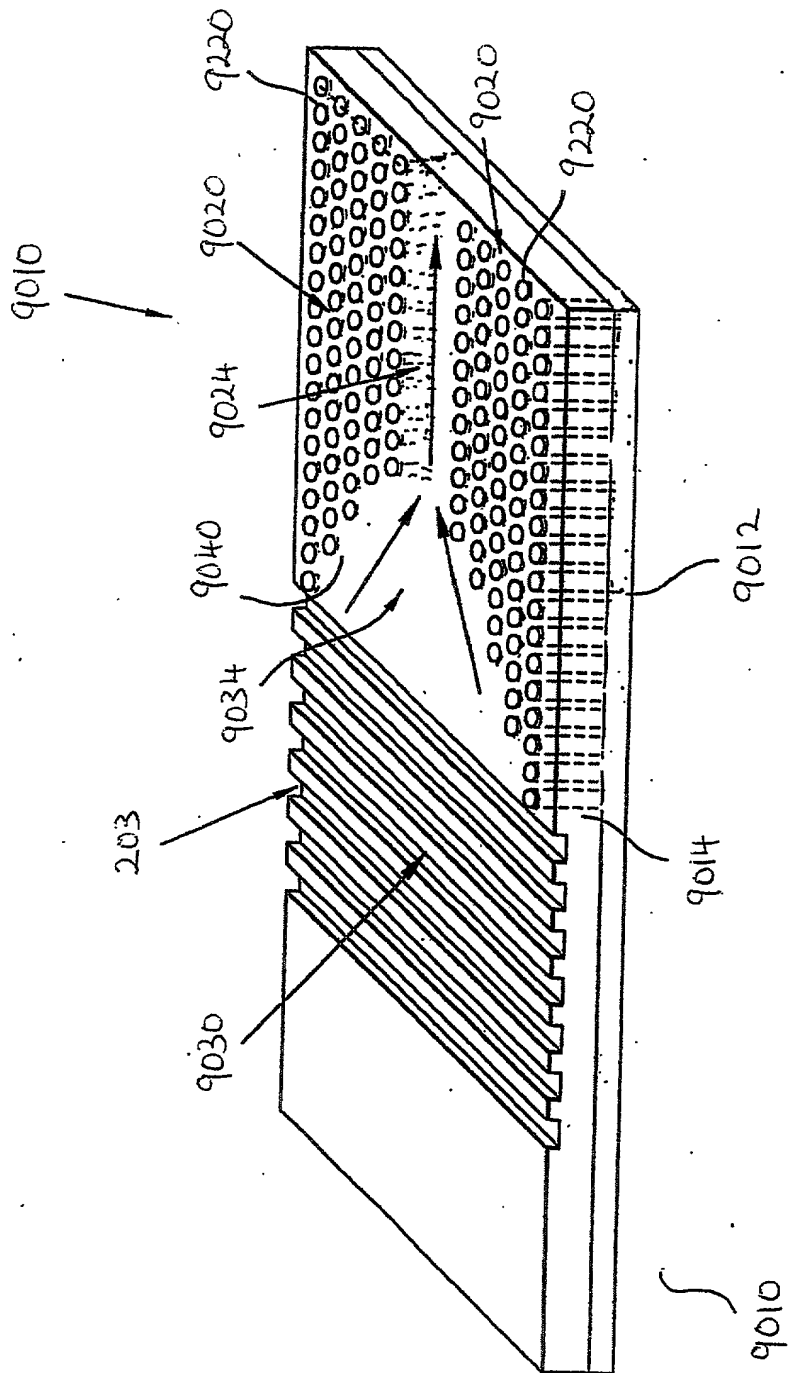


FIG. 83



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FIG. 84

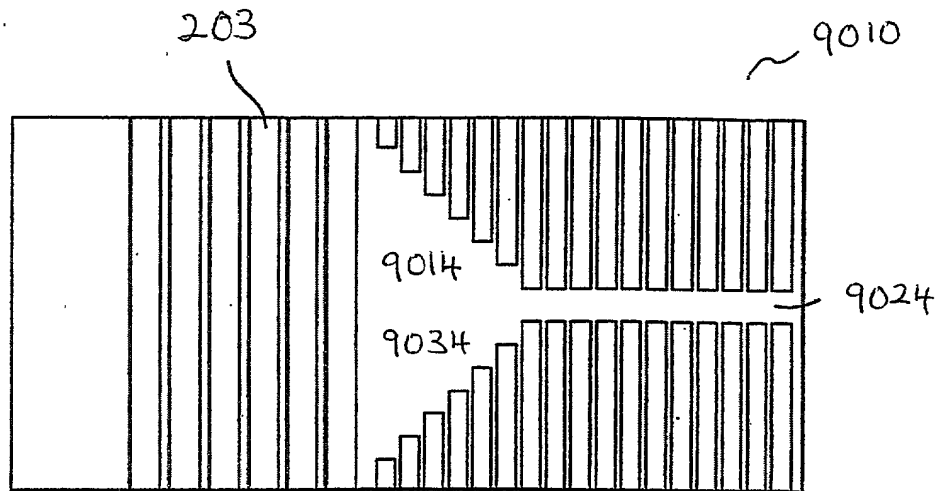


FIG. 85

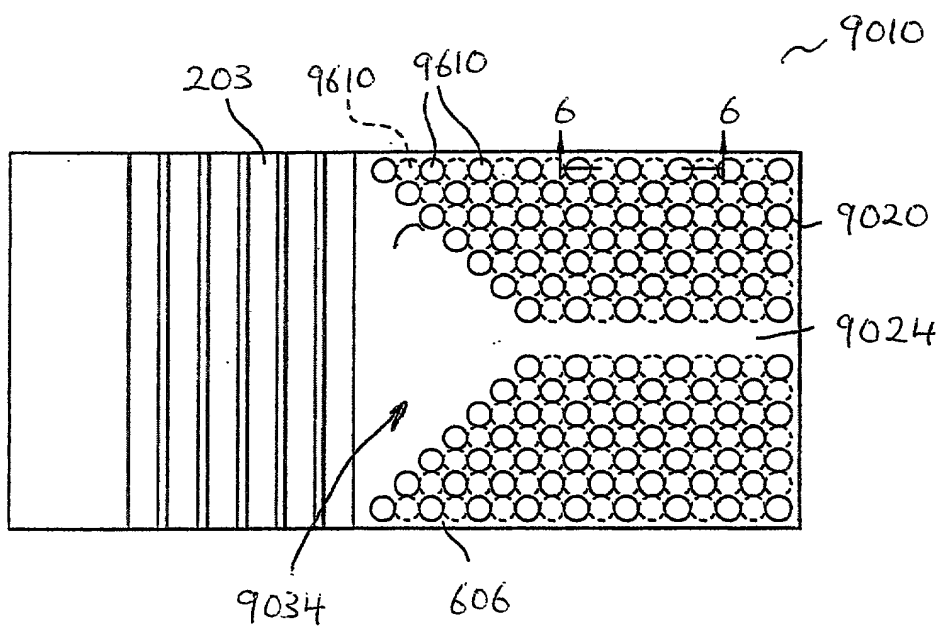


FIG. 86

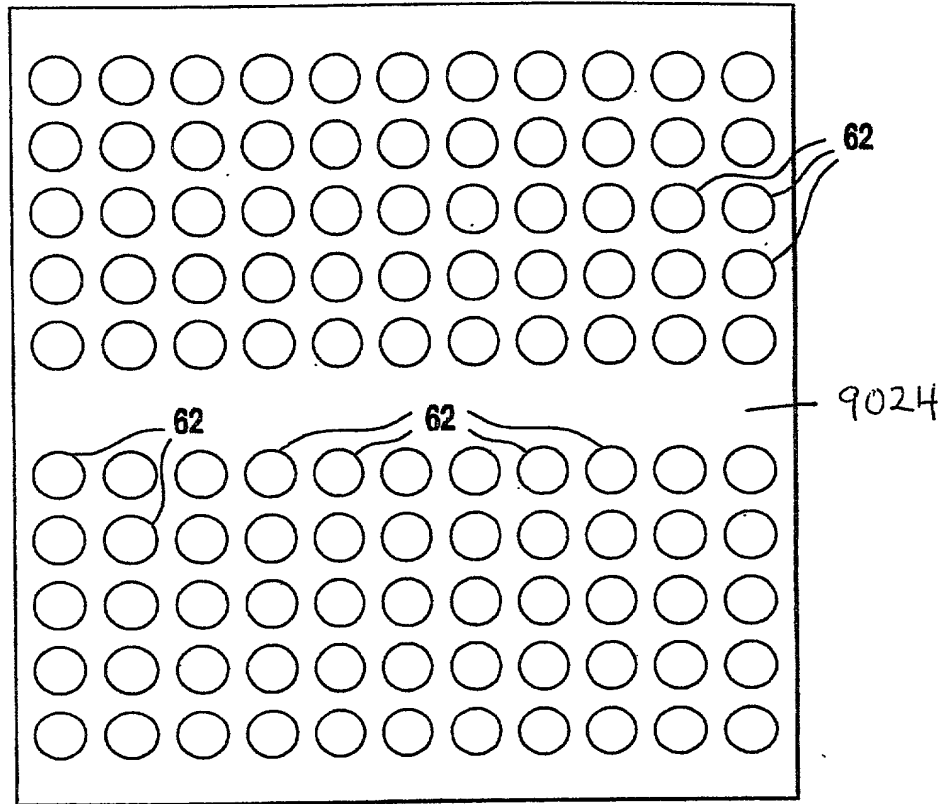


FIG. 87

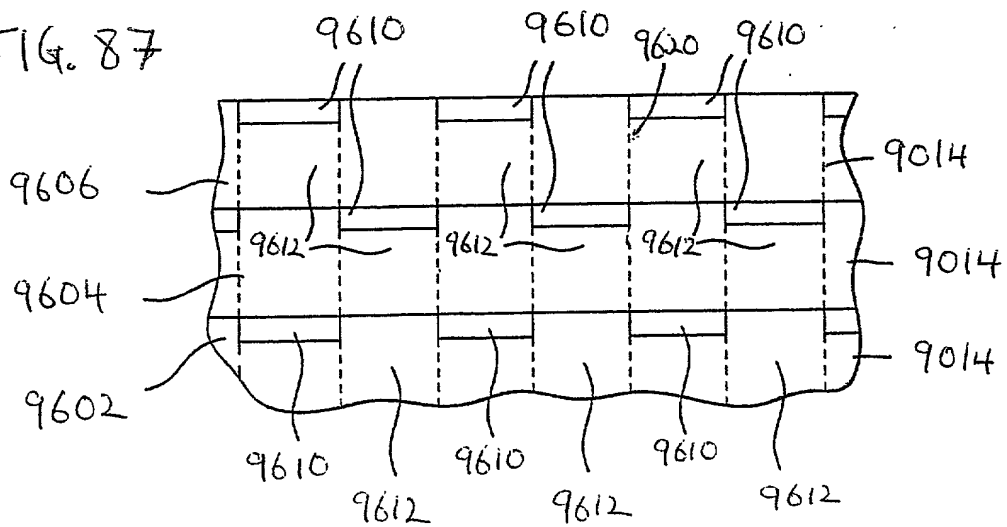
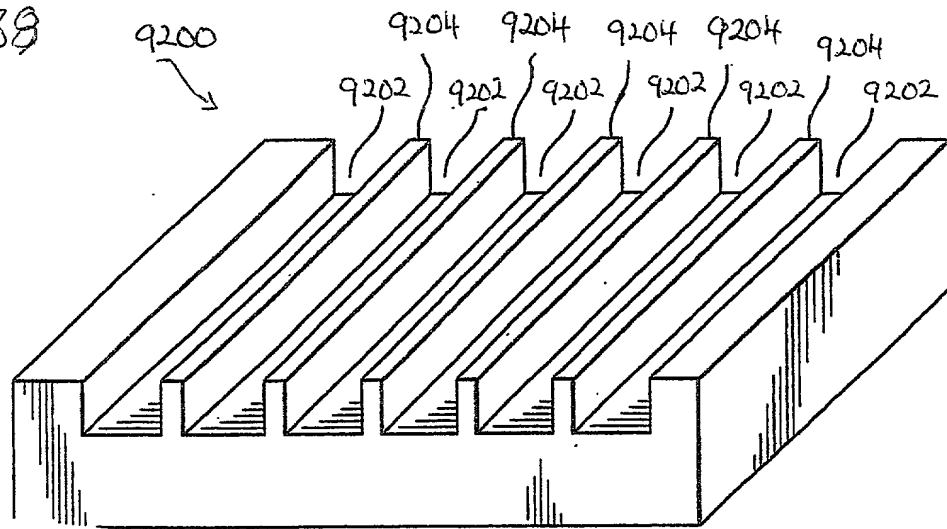


FIG. 88



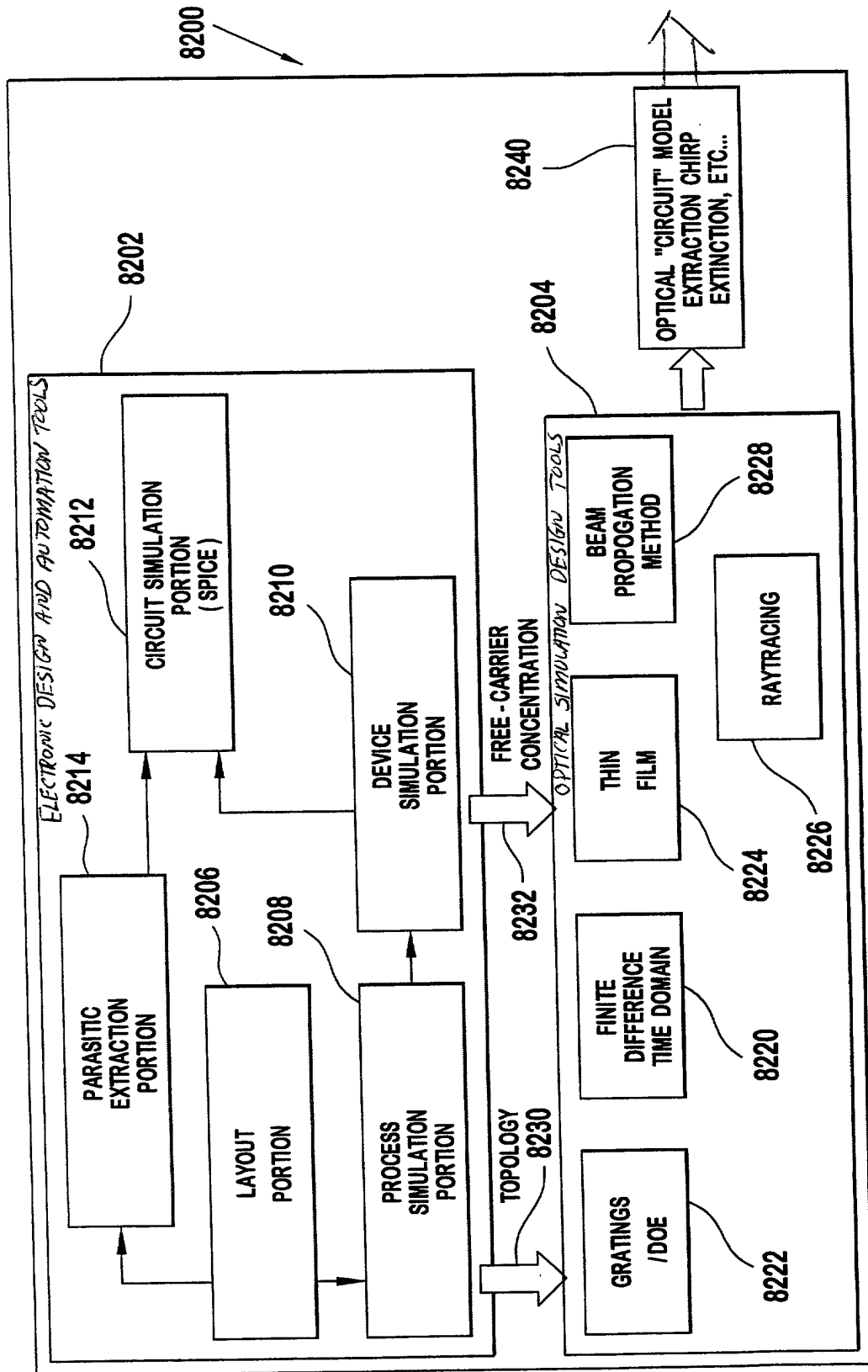
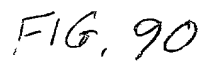


FIG. 8.9



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6502
↓

9002
↗

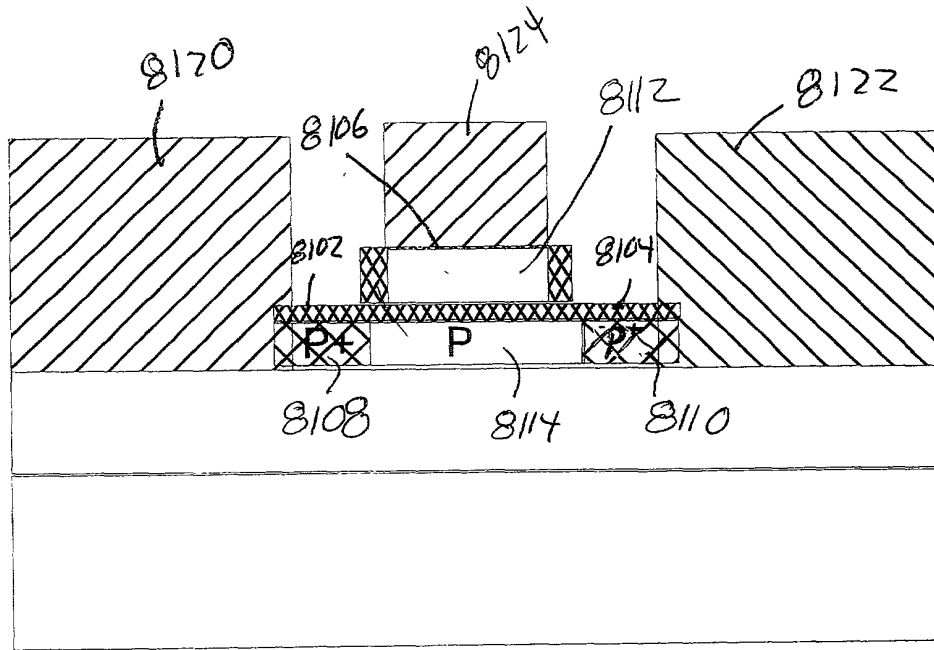


FIG. 91

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